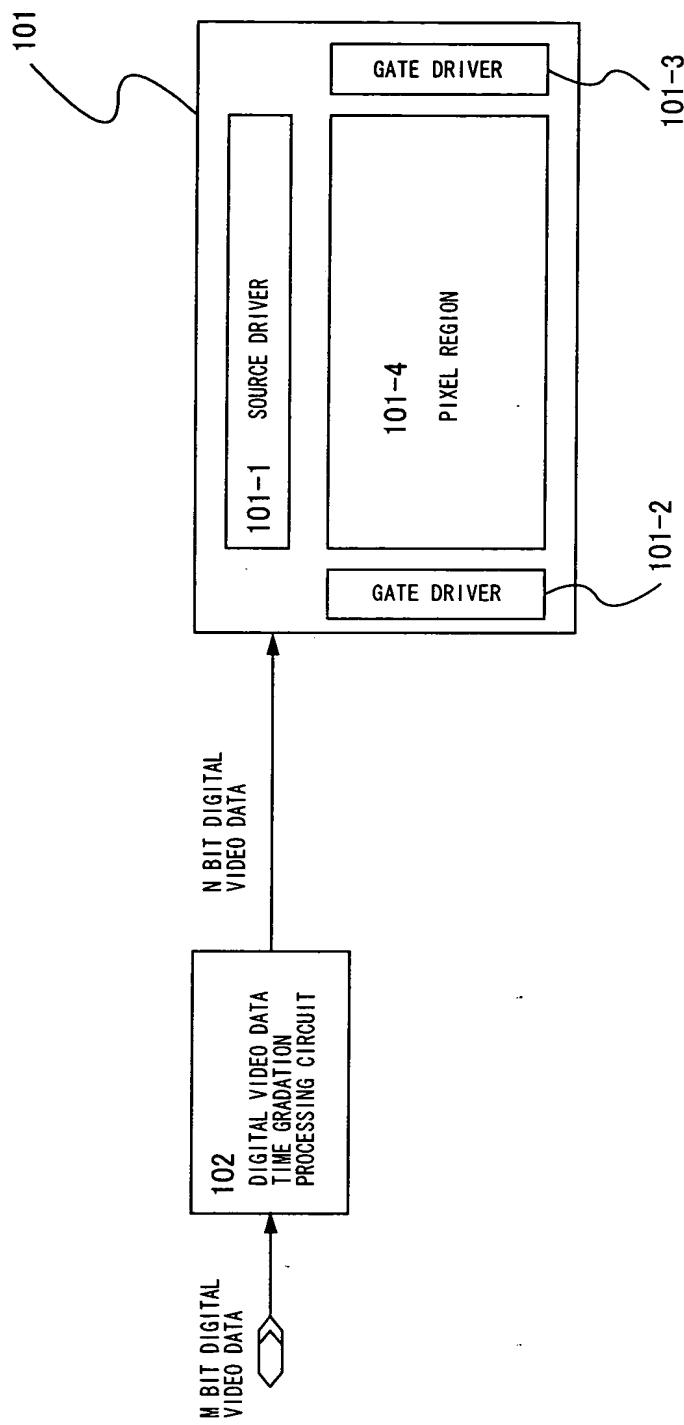


FIG. 1



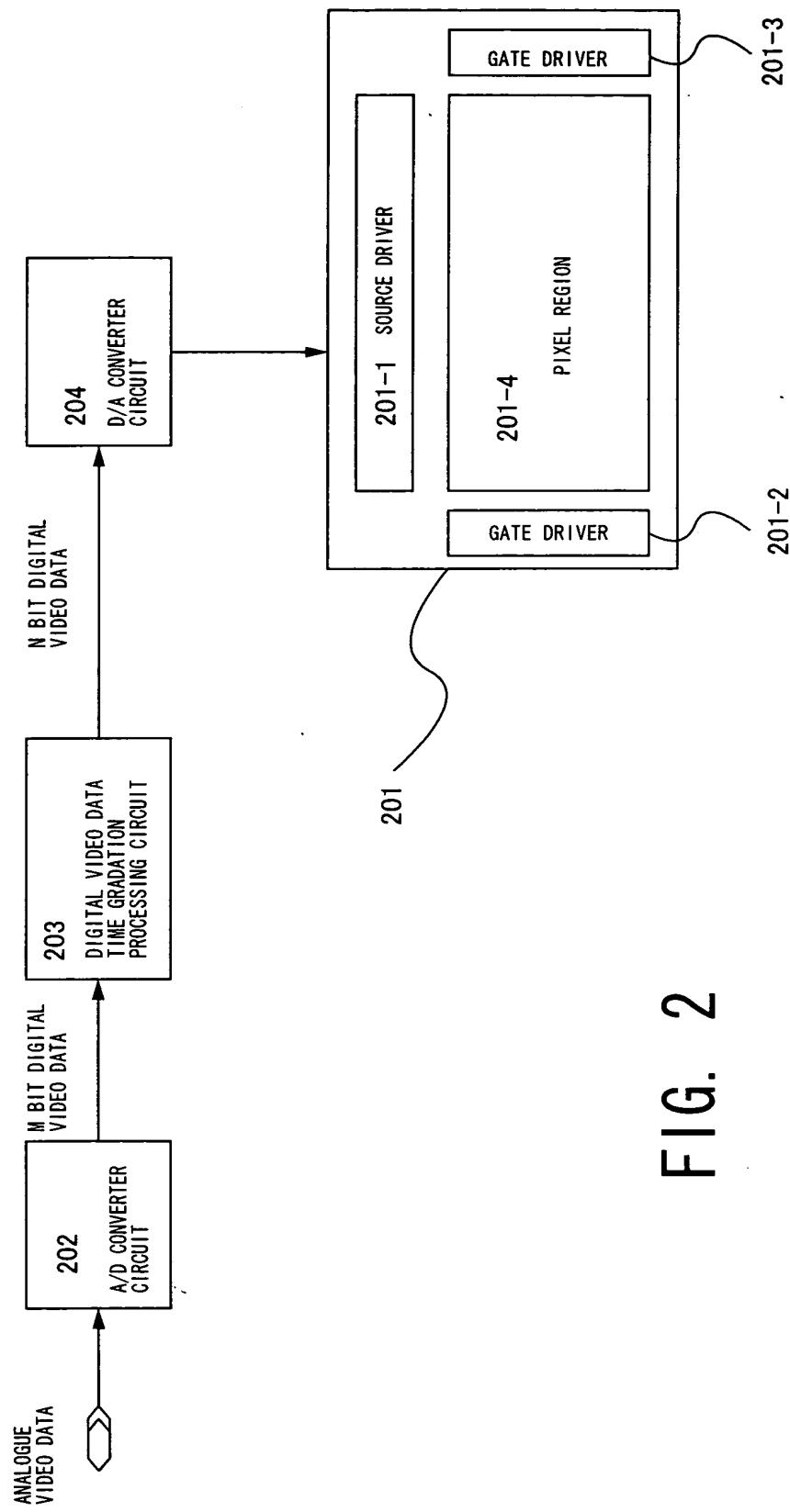
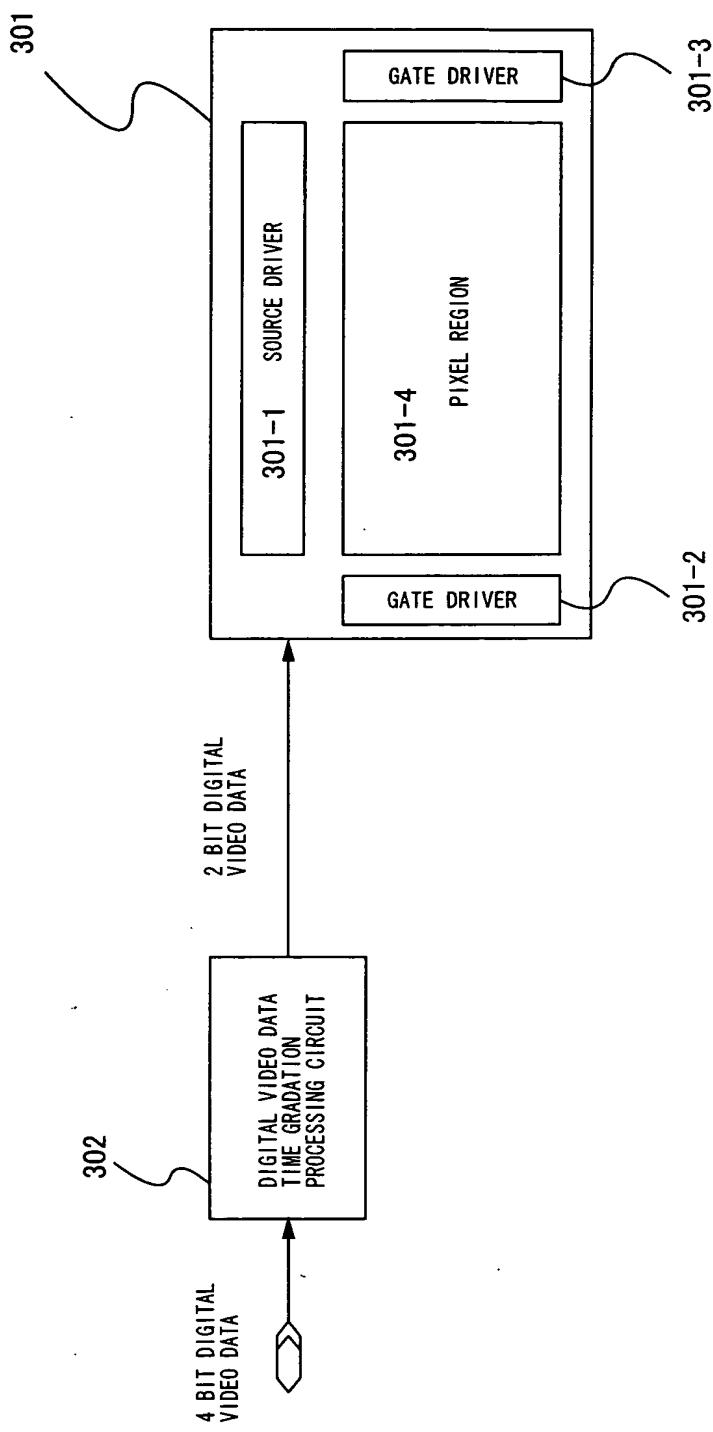


FIG. 2

FIG. 3



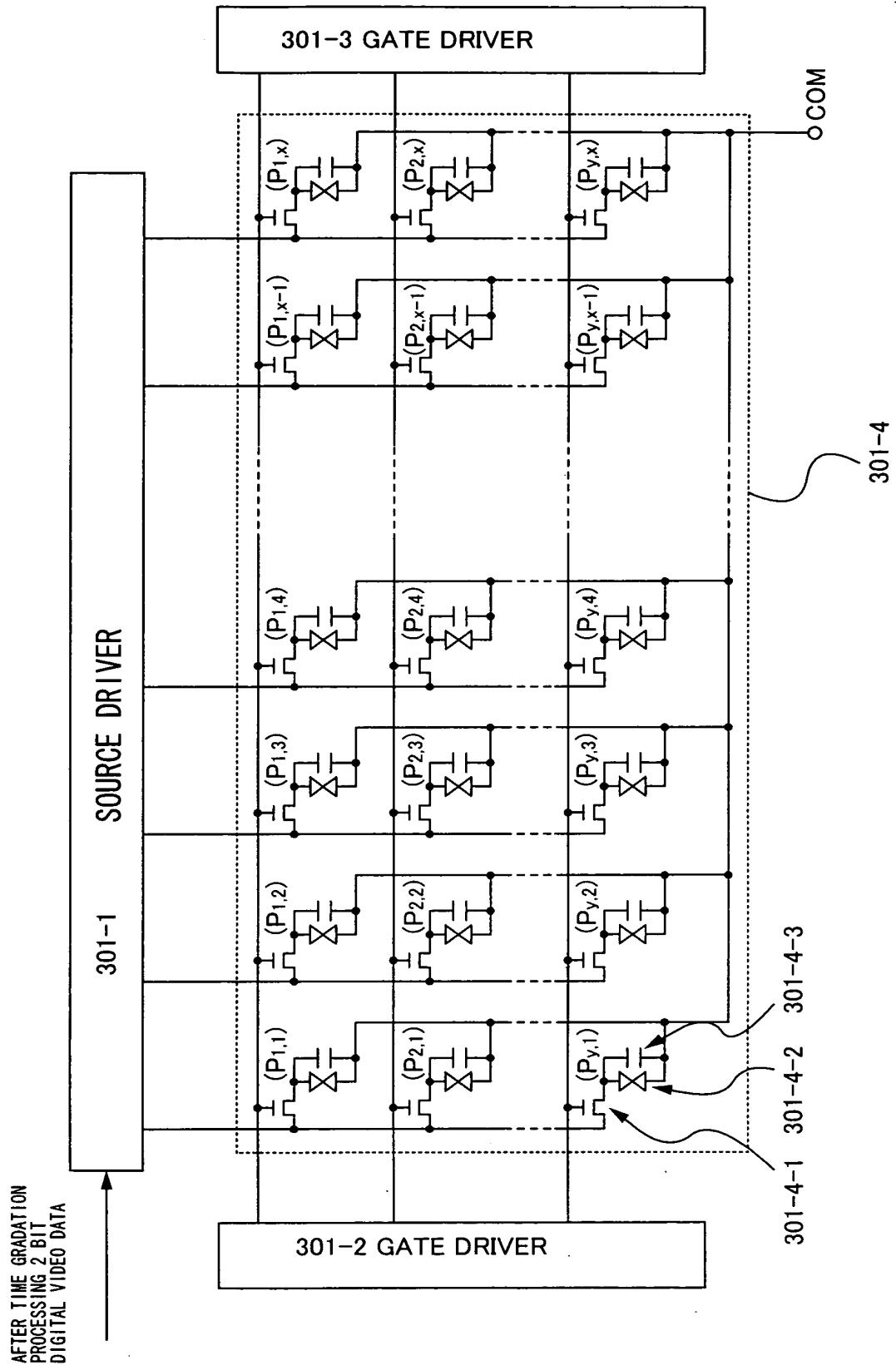


FIG. 4

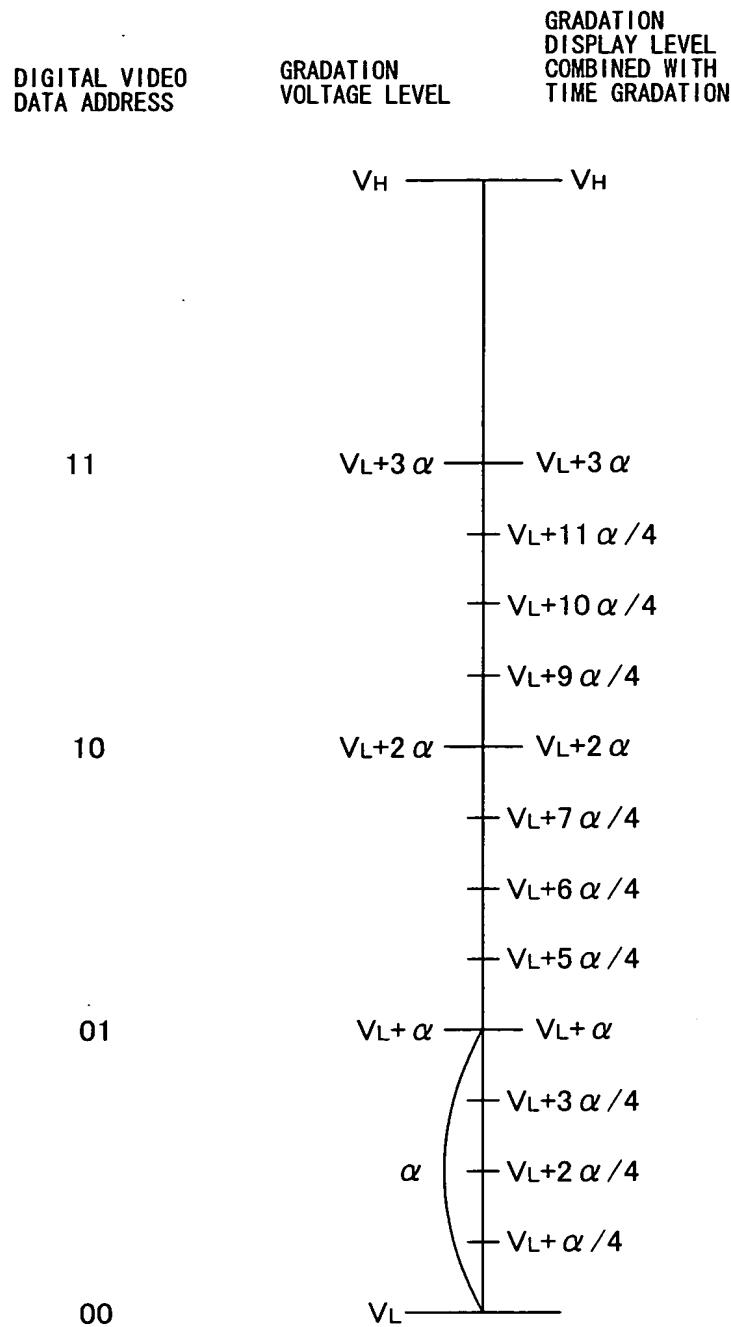
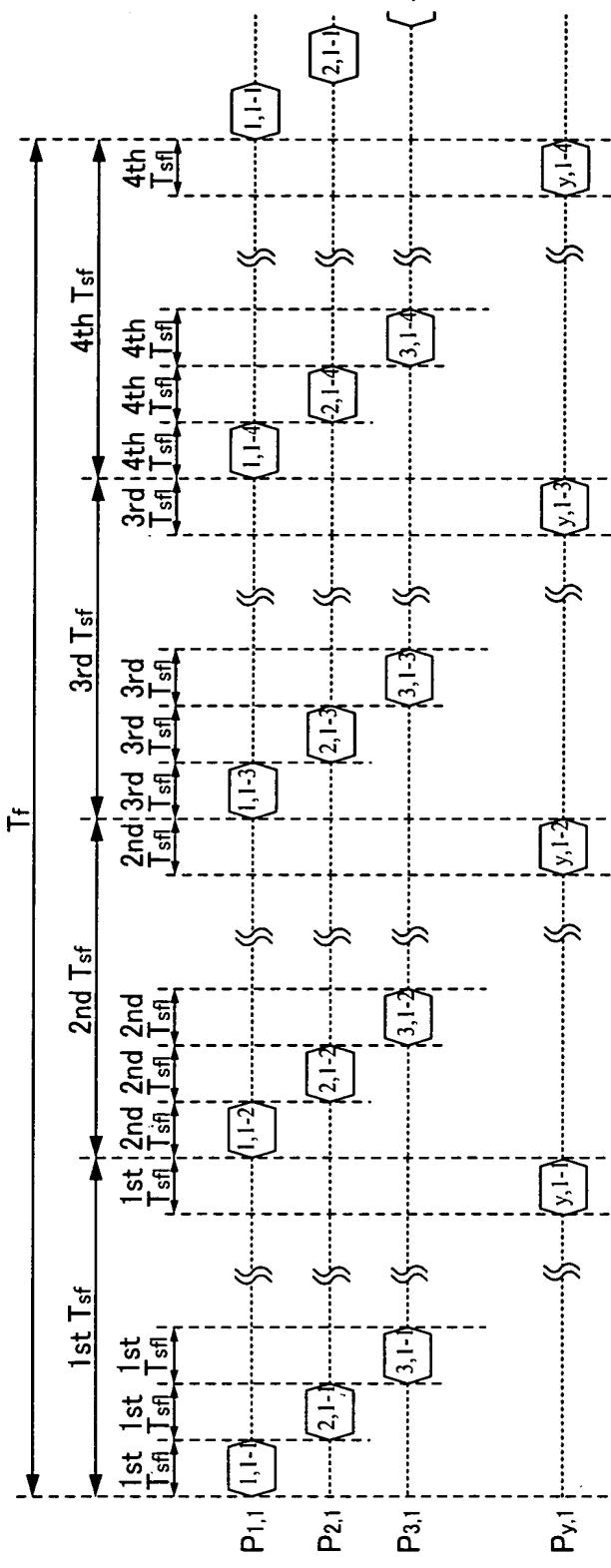


FIG. 5

FIG. 6



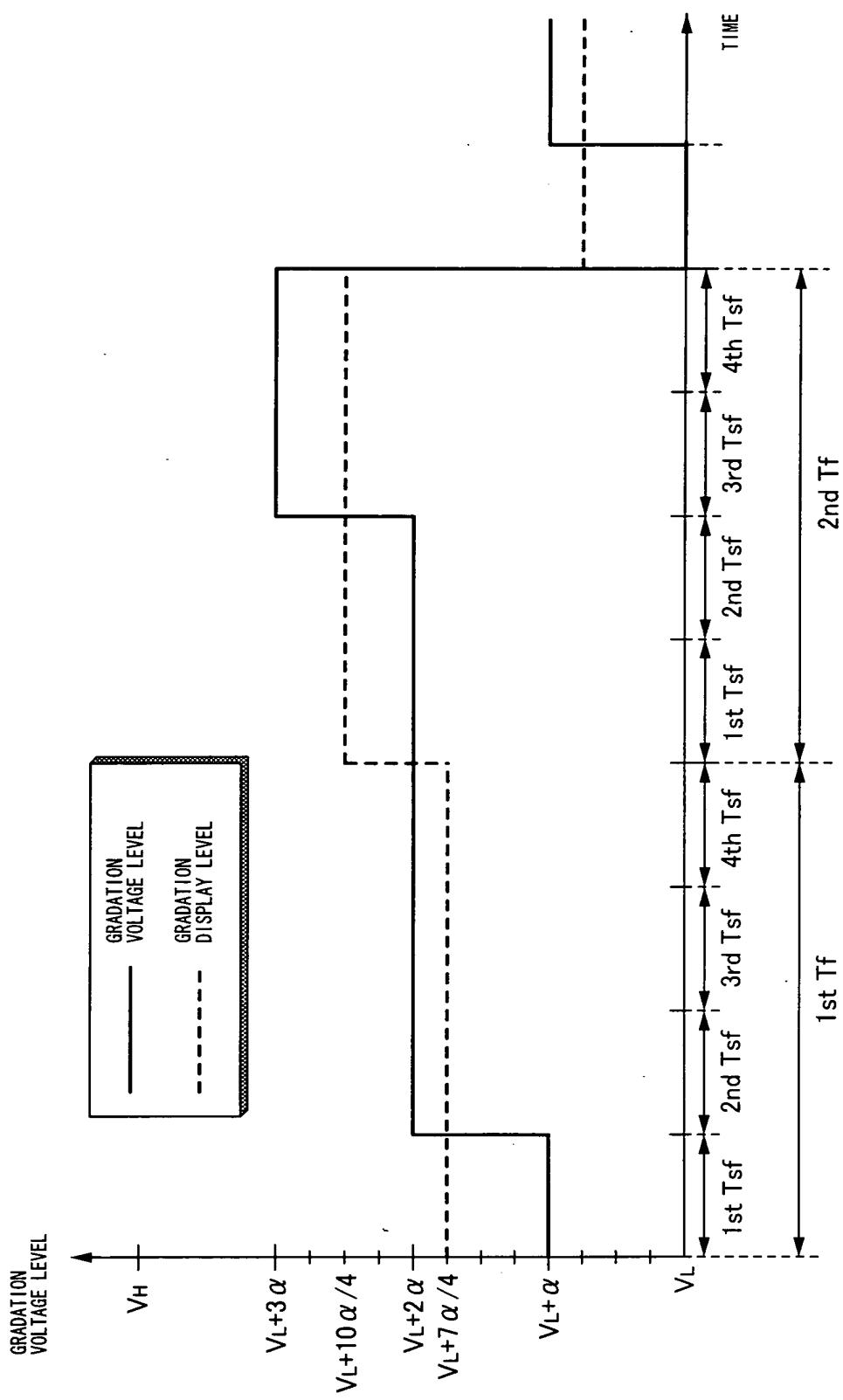
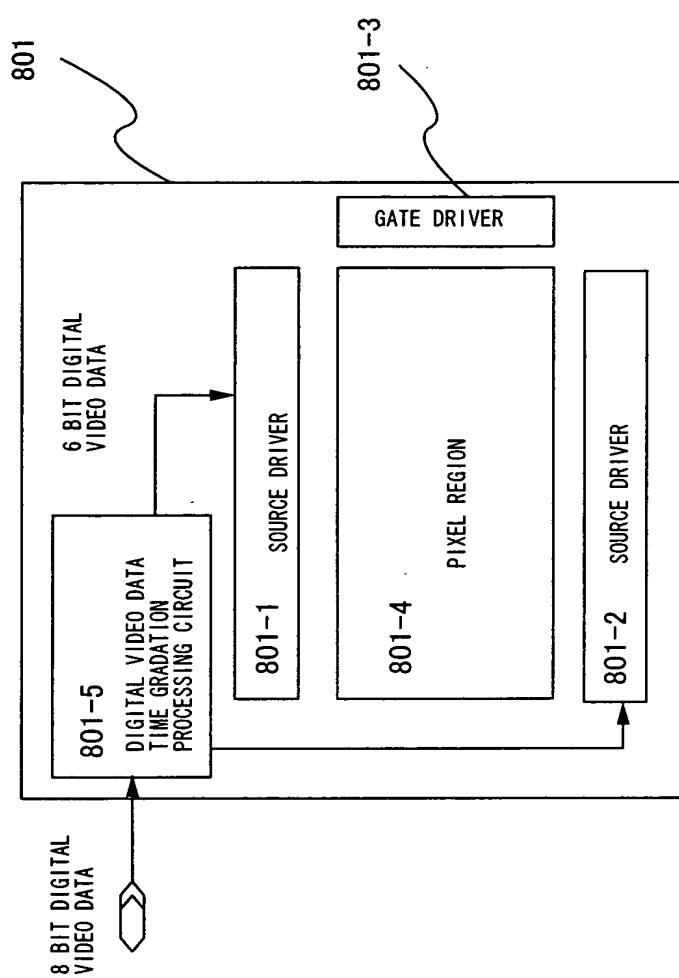


FIG. 7

FIG. 8



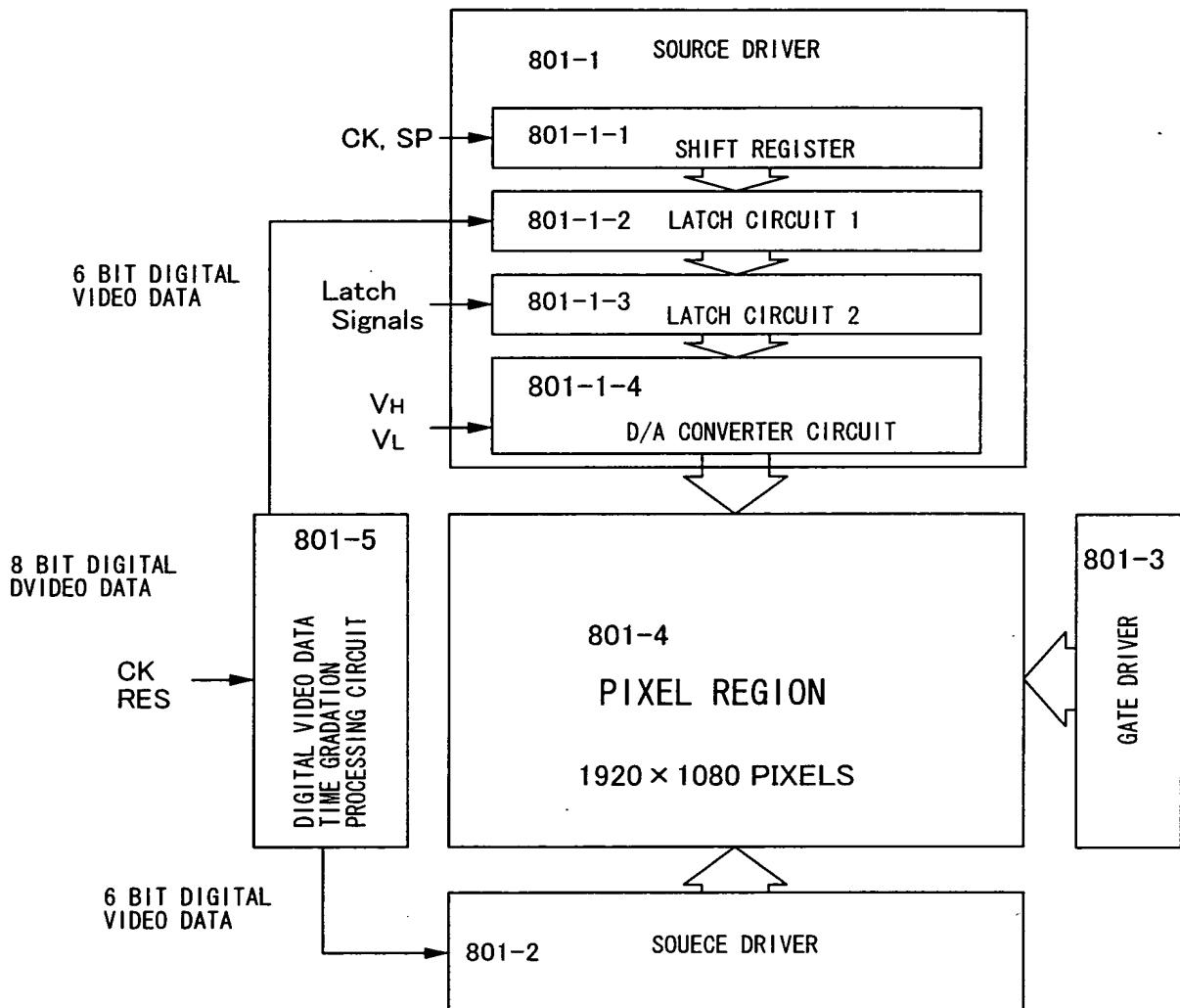


FIG. 9

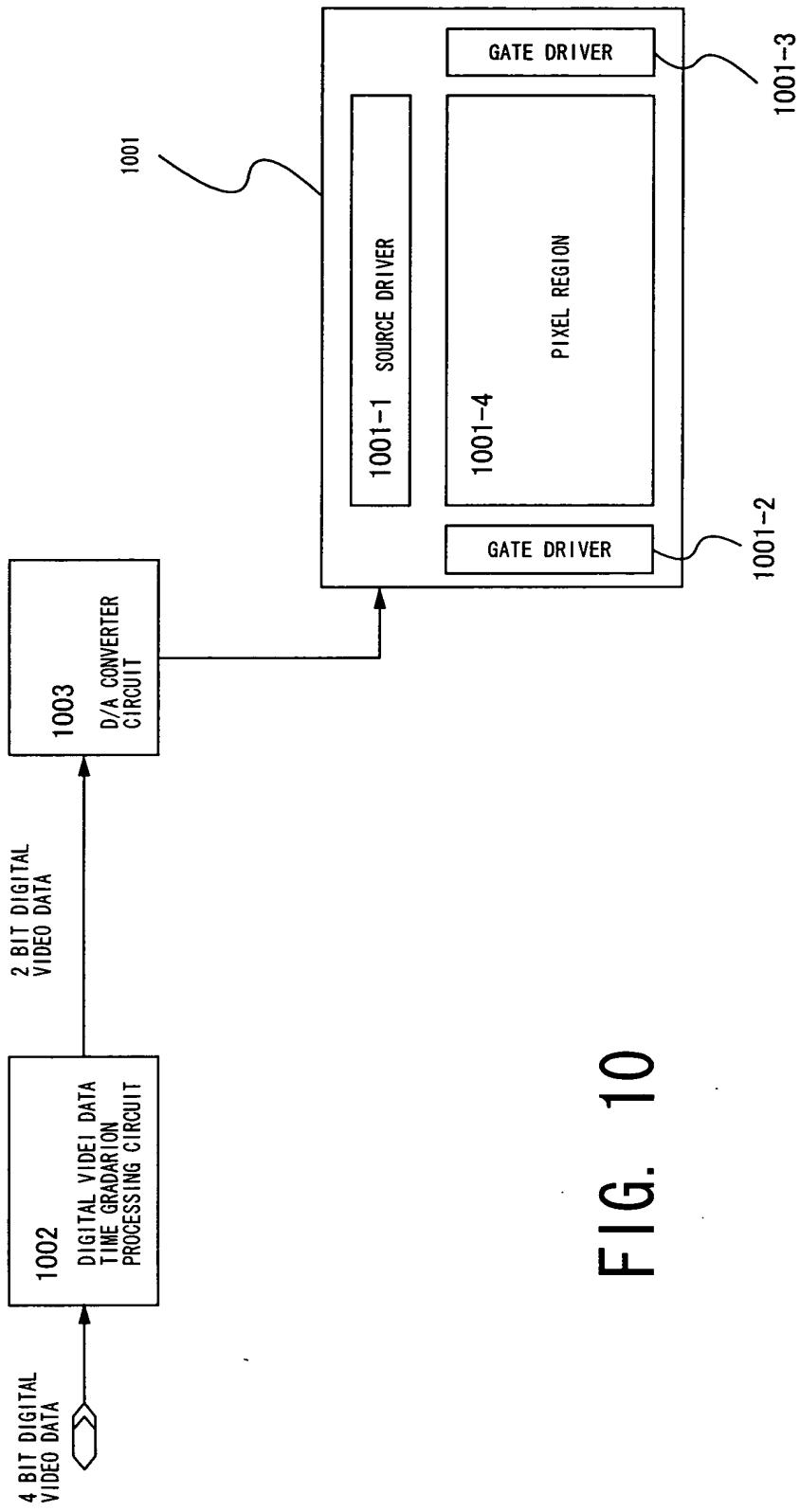


FIG. 10

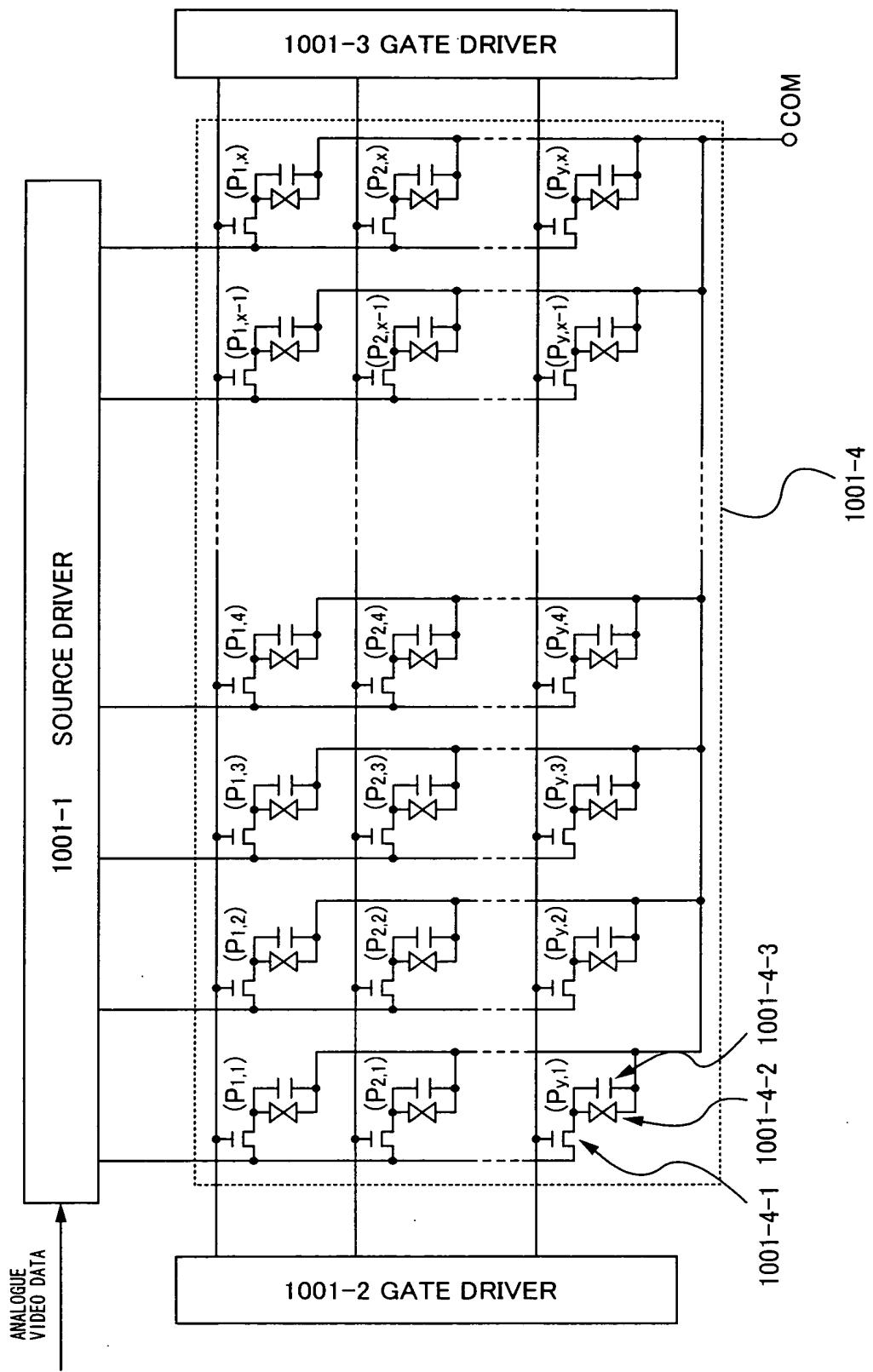
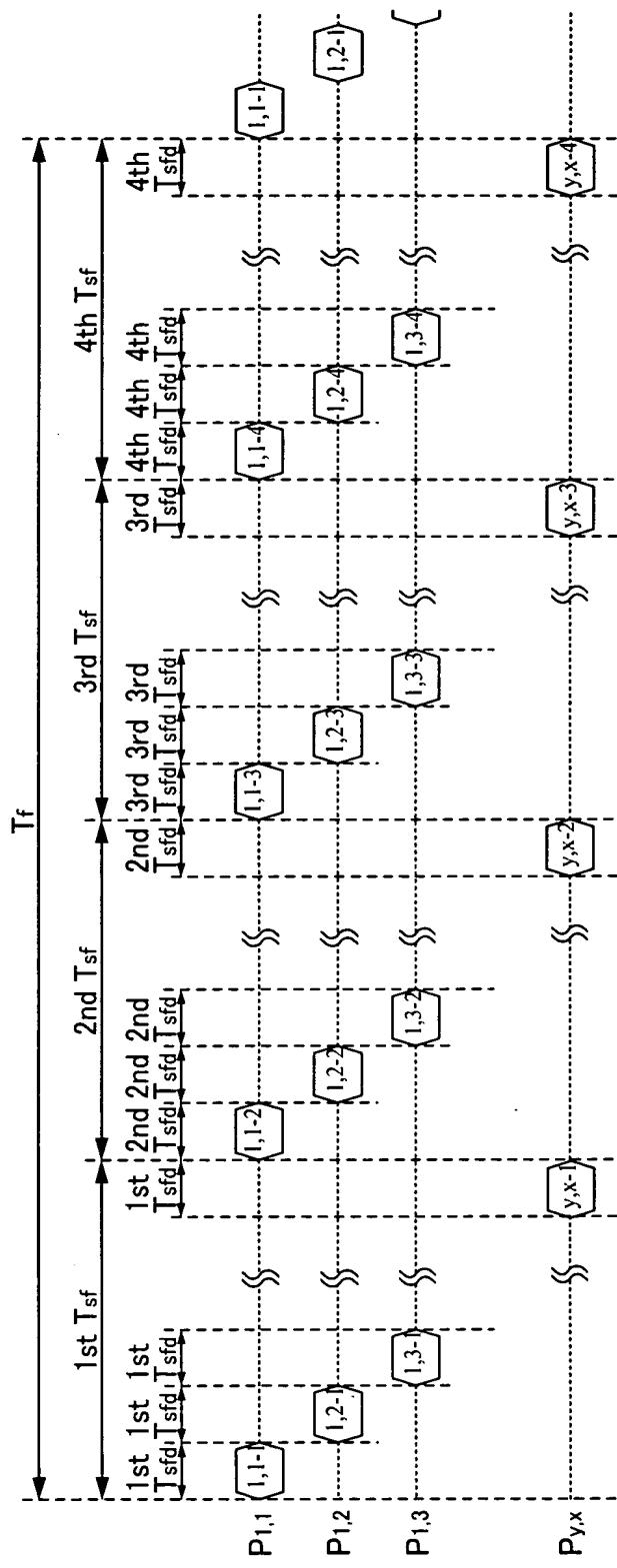


FIG. 11

FIG. 12



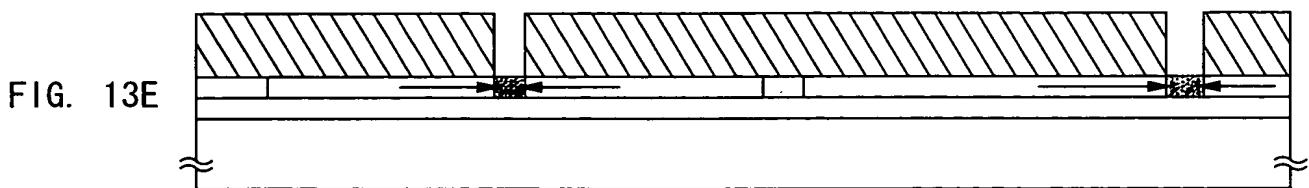
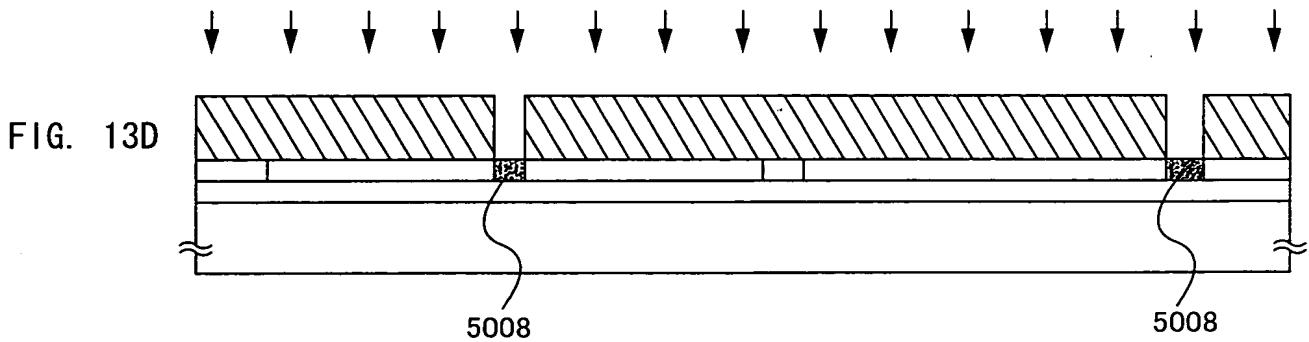
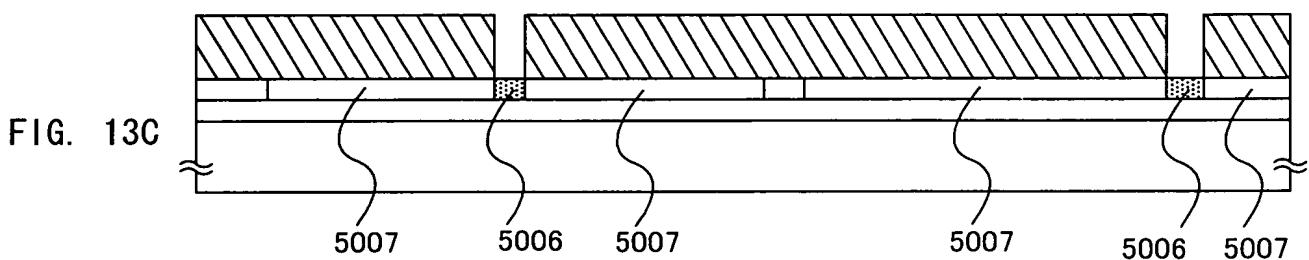
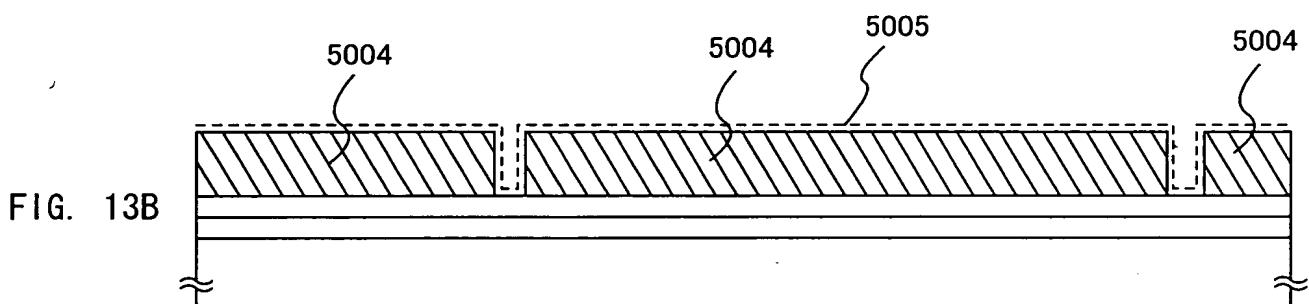
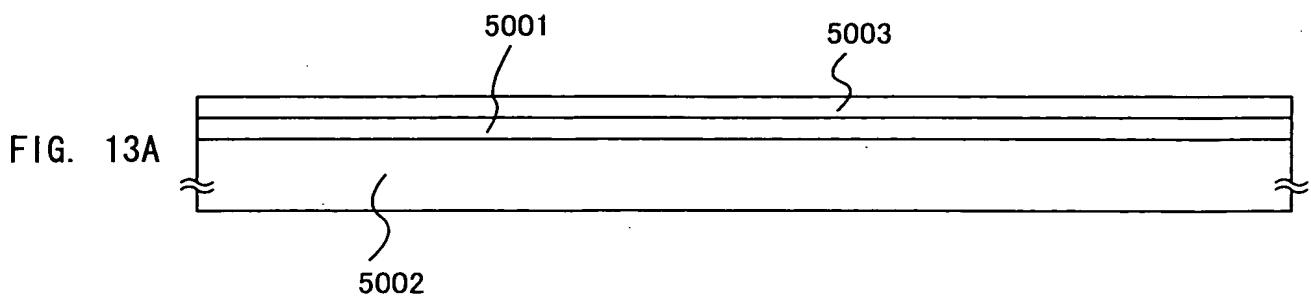


FIG. 14A

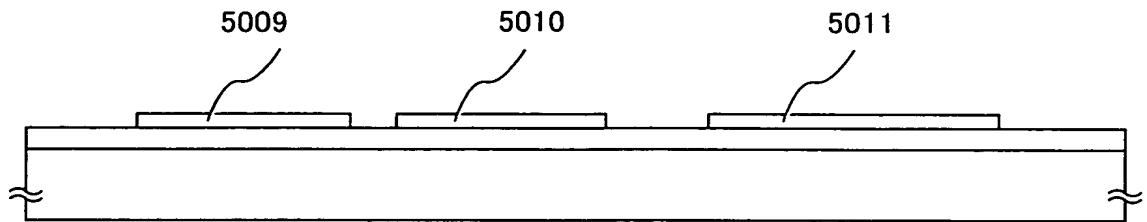


FIG. 14B

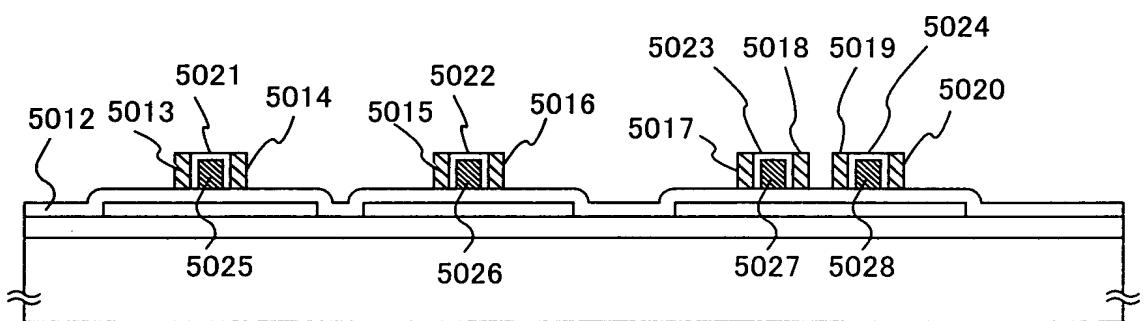


FIG. 14C

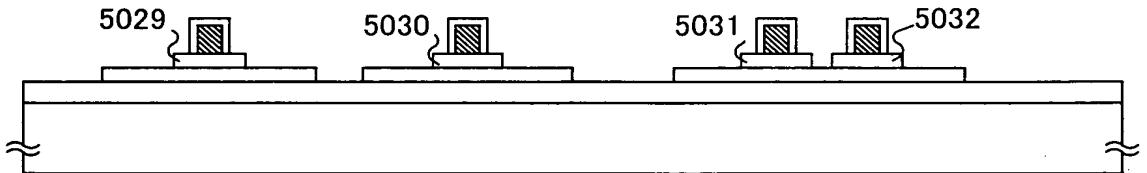


FIG. 15A

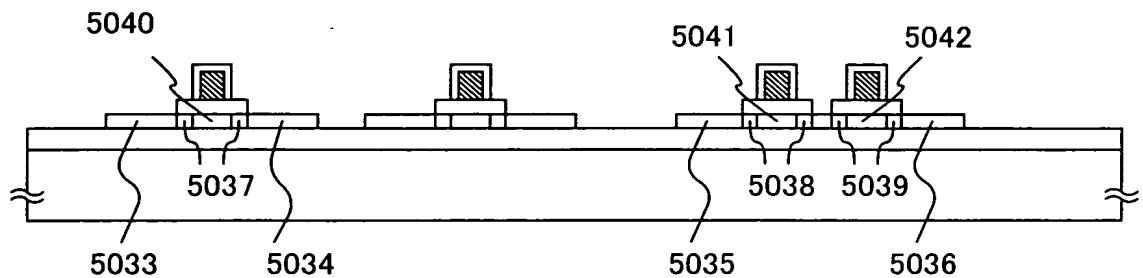


FIG. 15B

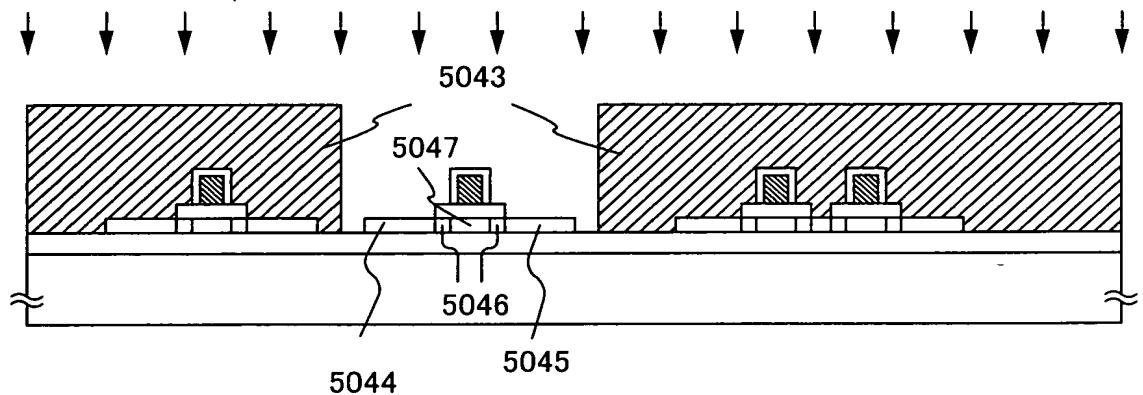
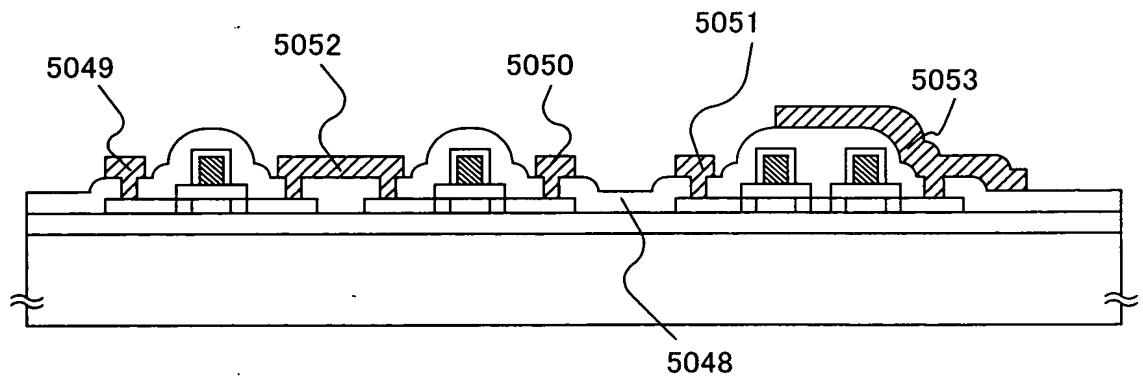
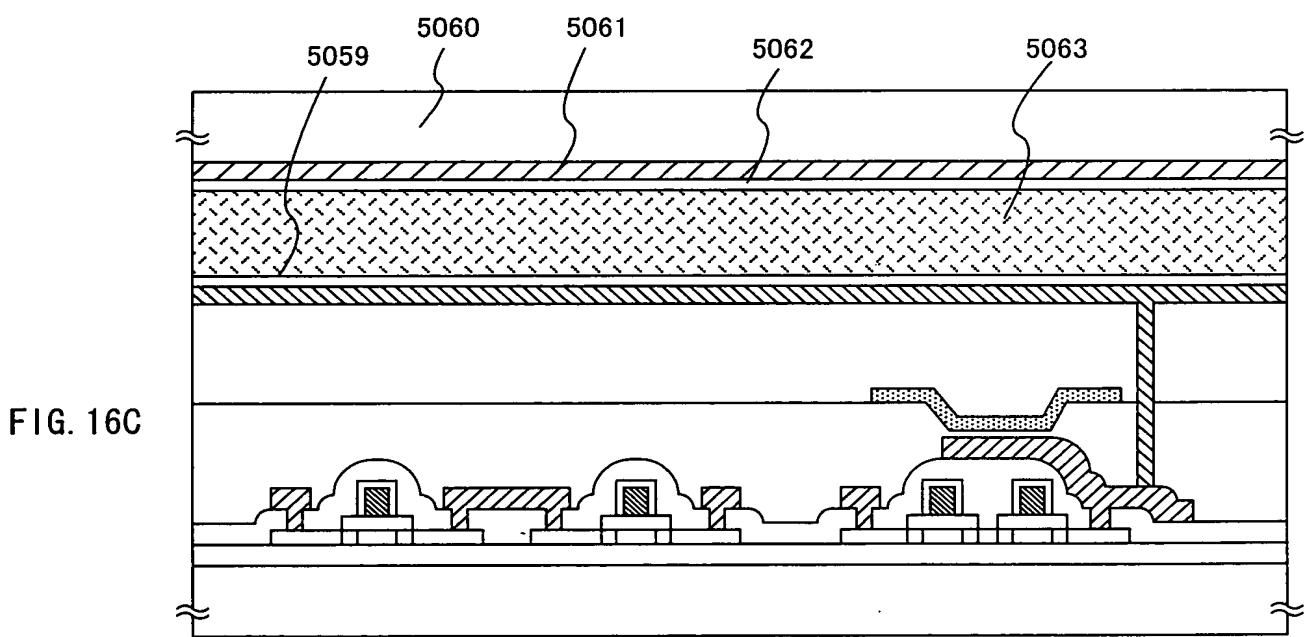
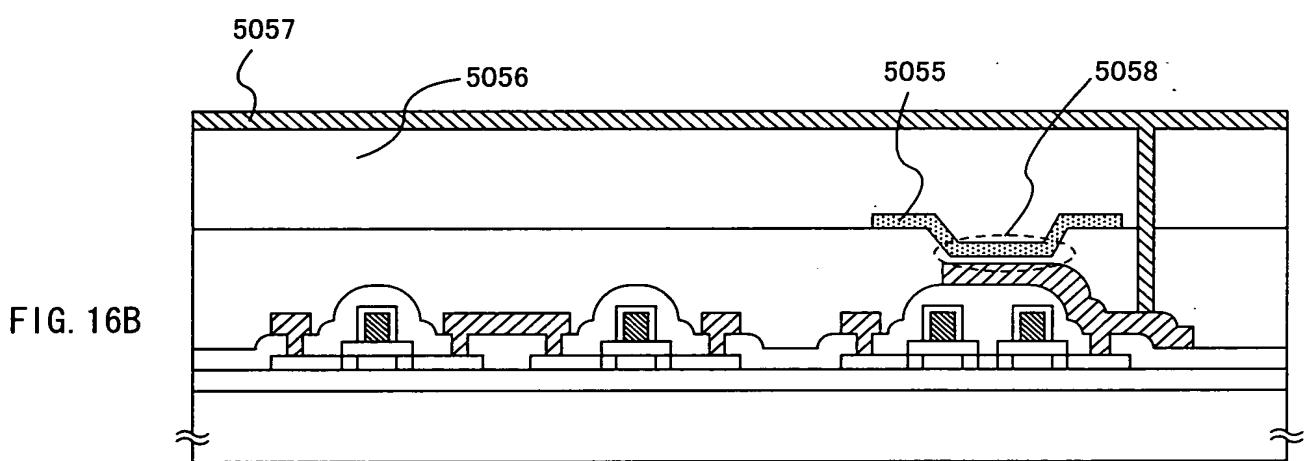
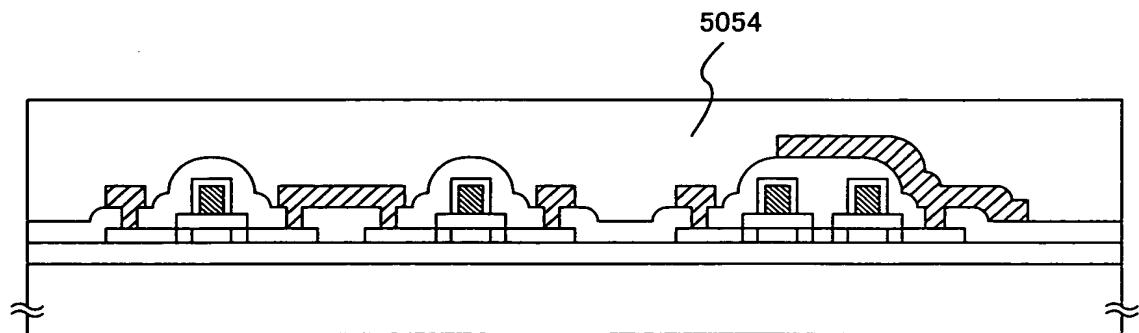
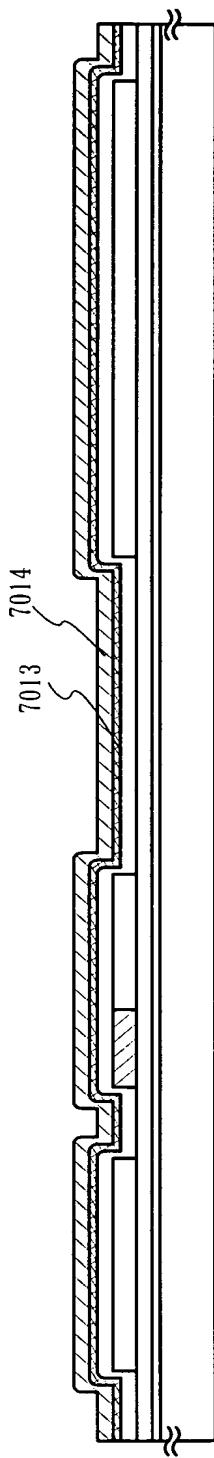
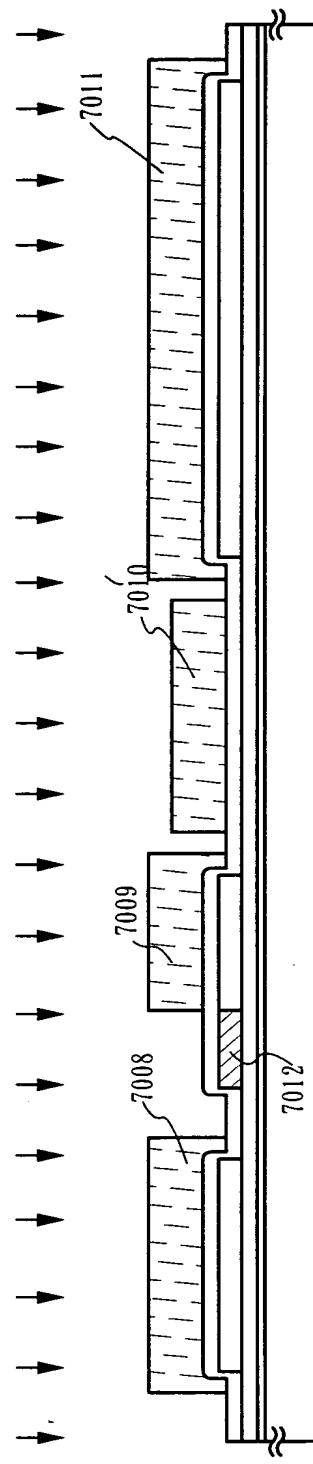
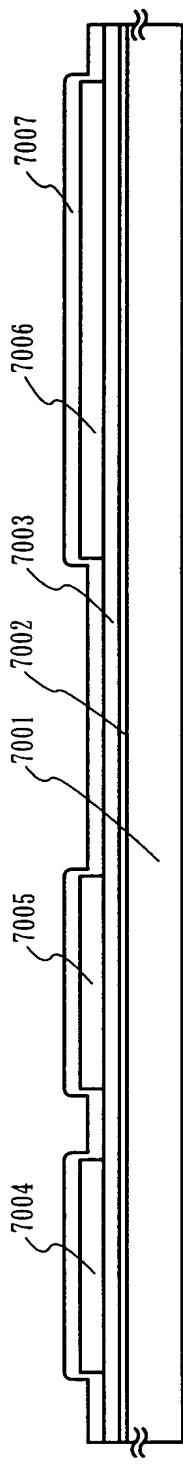
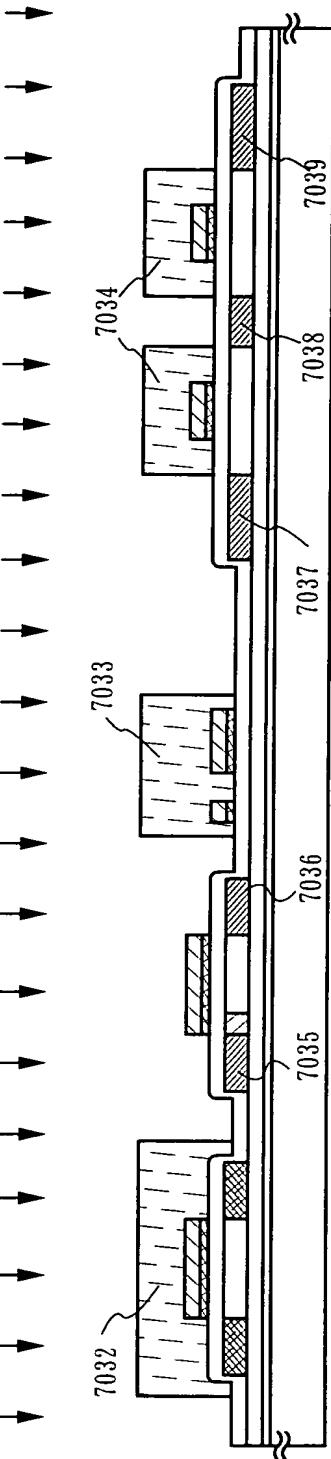
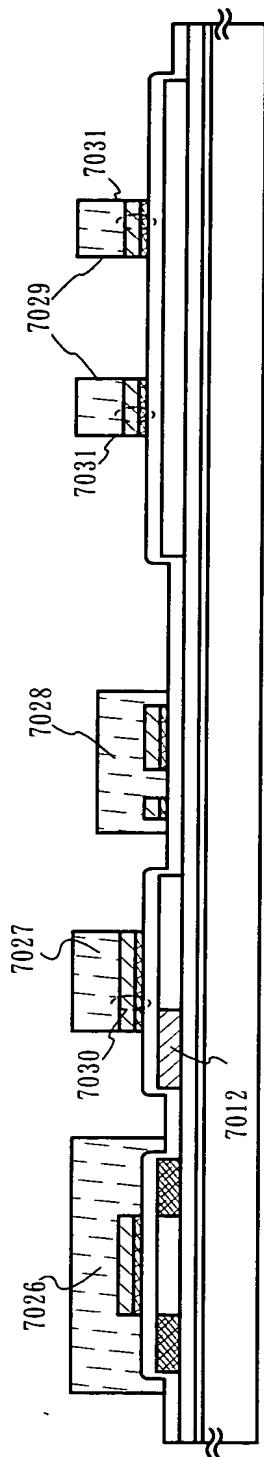
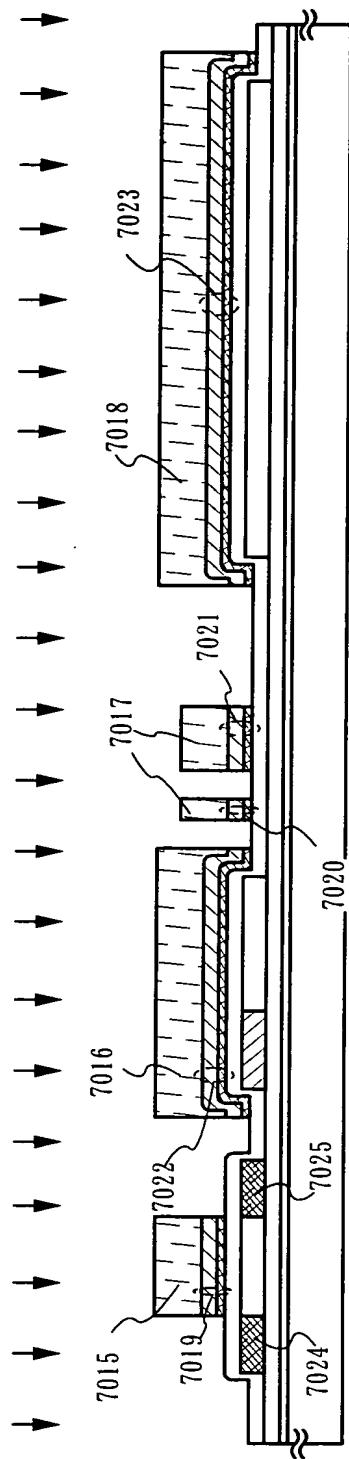


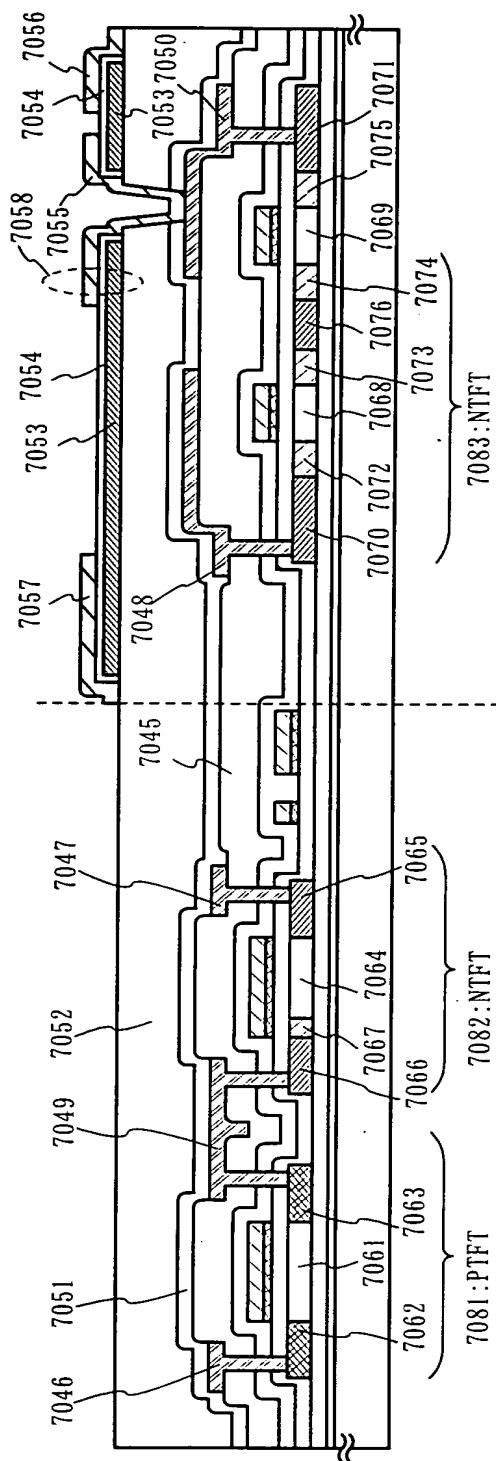
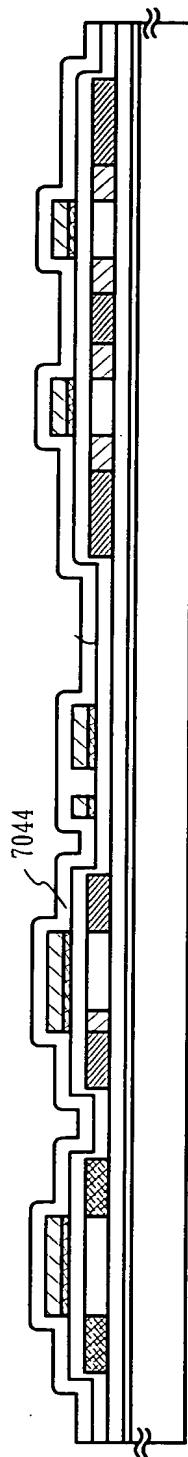
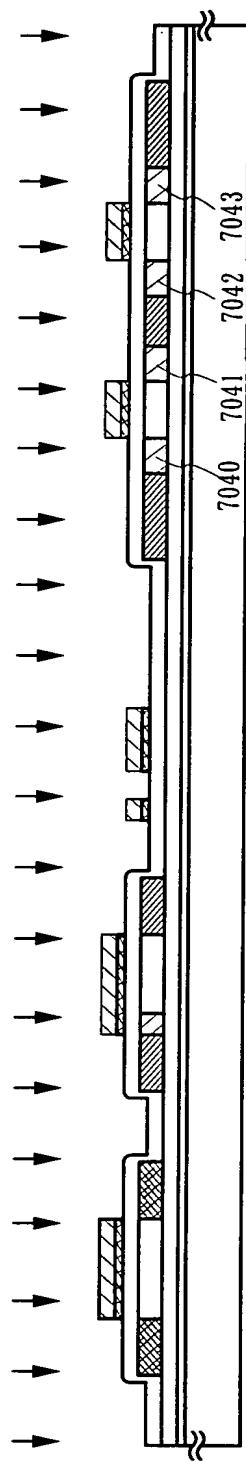
FIG. 15C





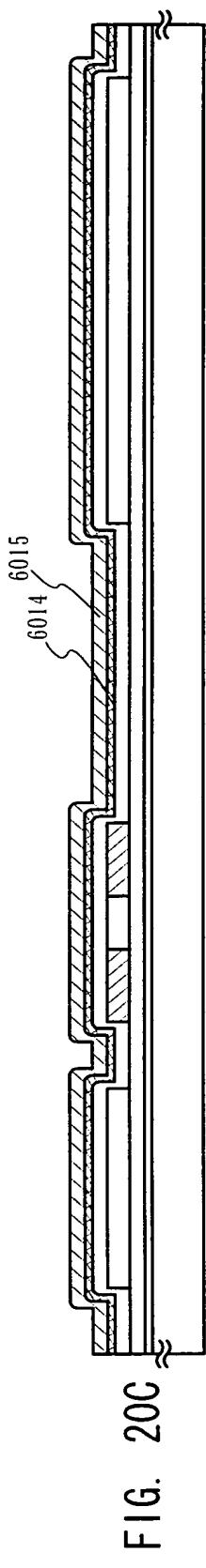
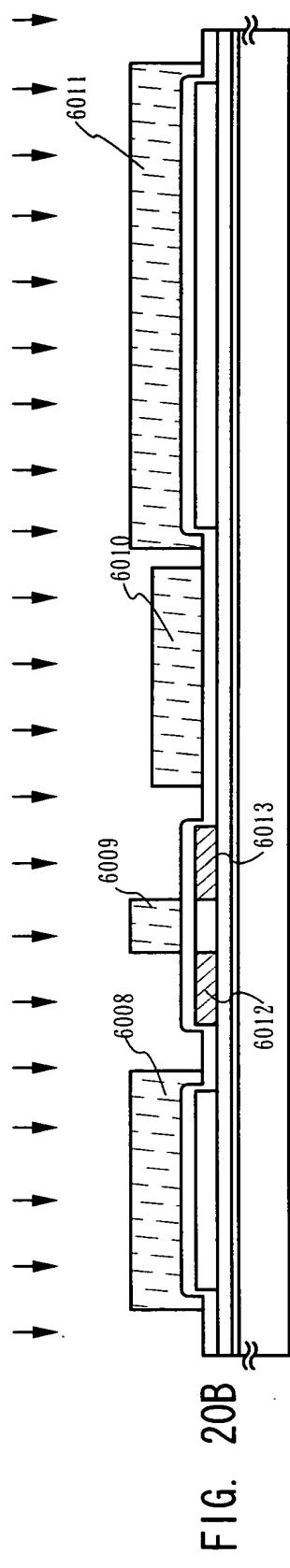
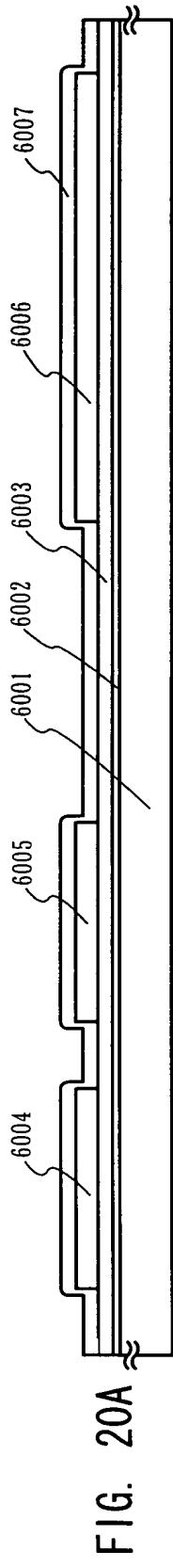






CMOS CIRCUIT

PIXEL REGION



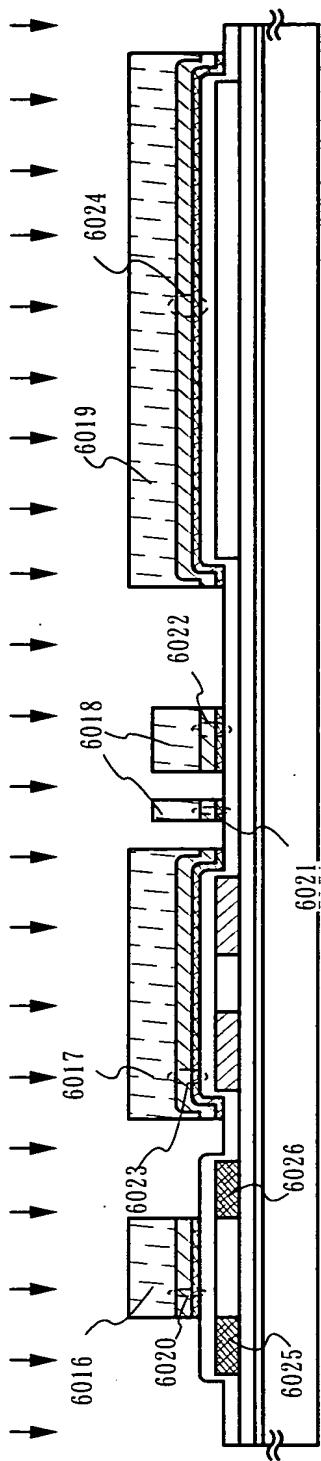


FIG. 21A

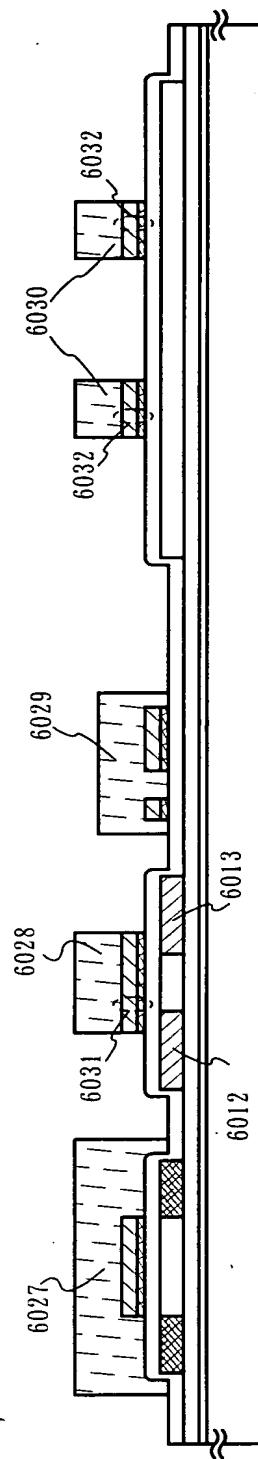


FIG. 21B

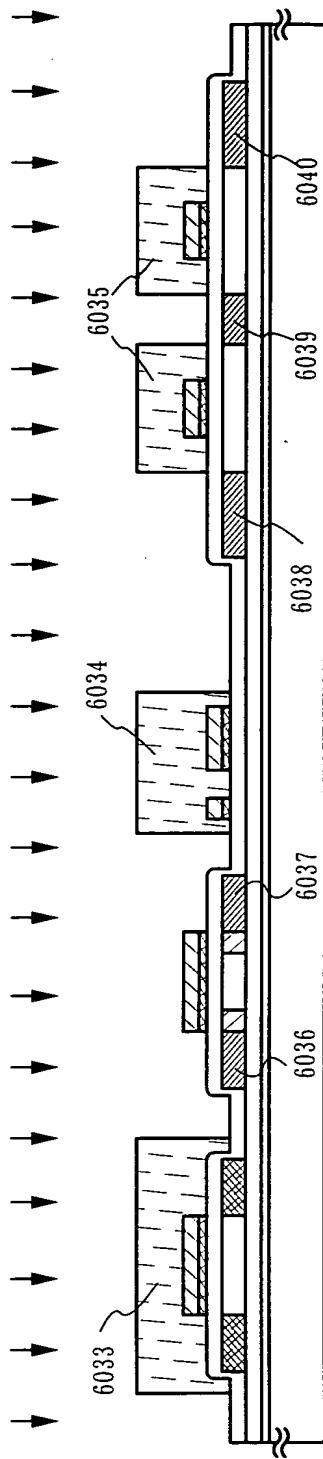
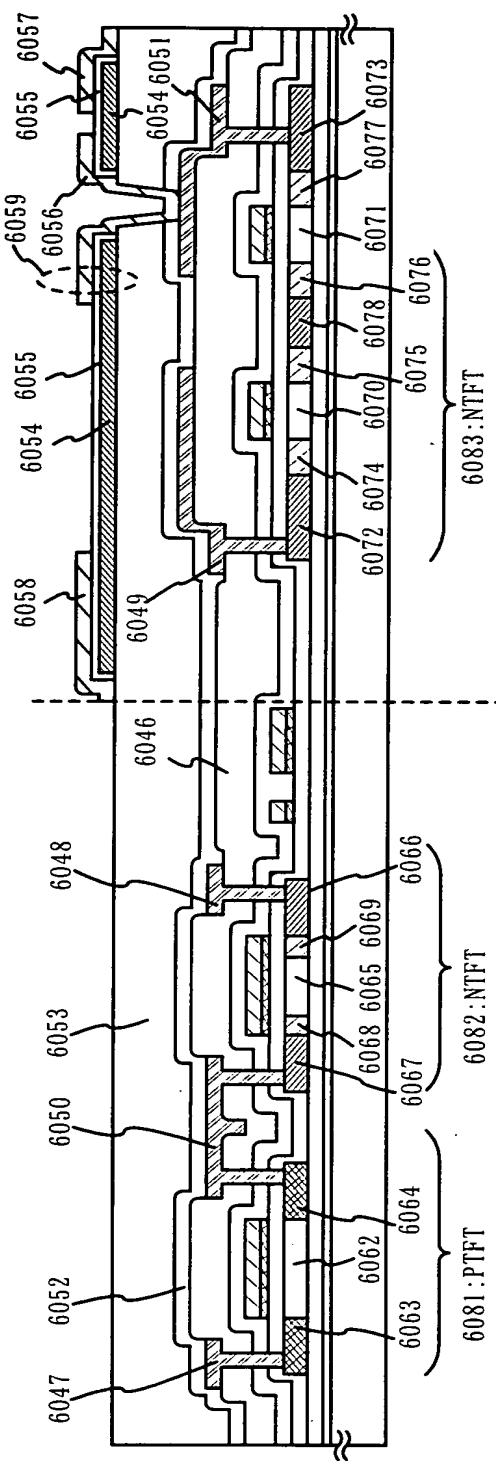
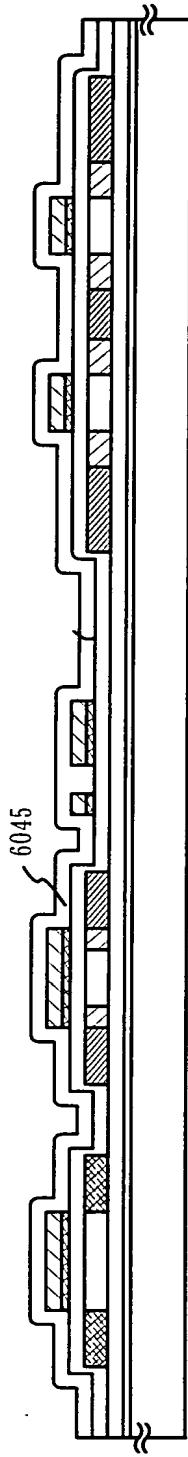
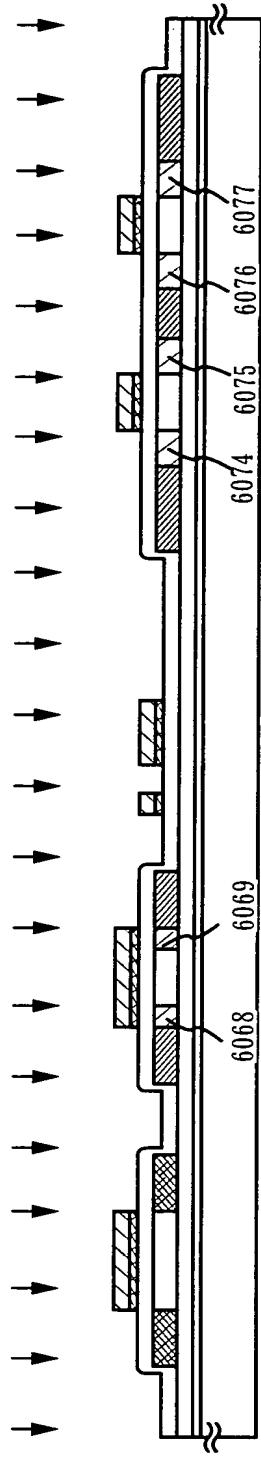


FIG. 21C



CMOS CIRCUIT

PIXEL REGION

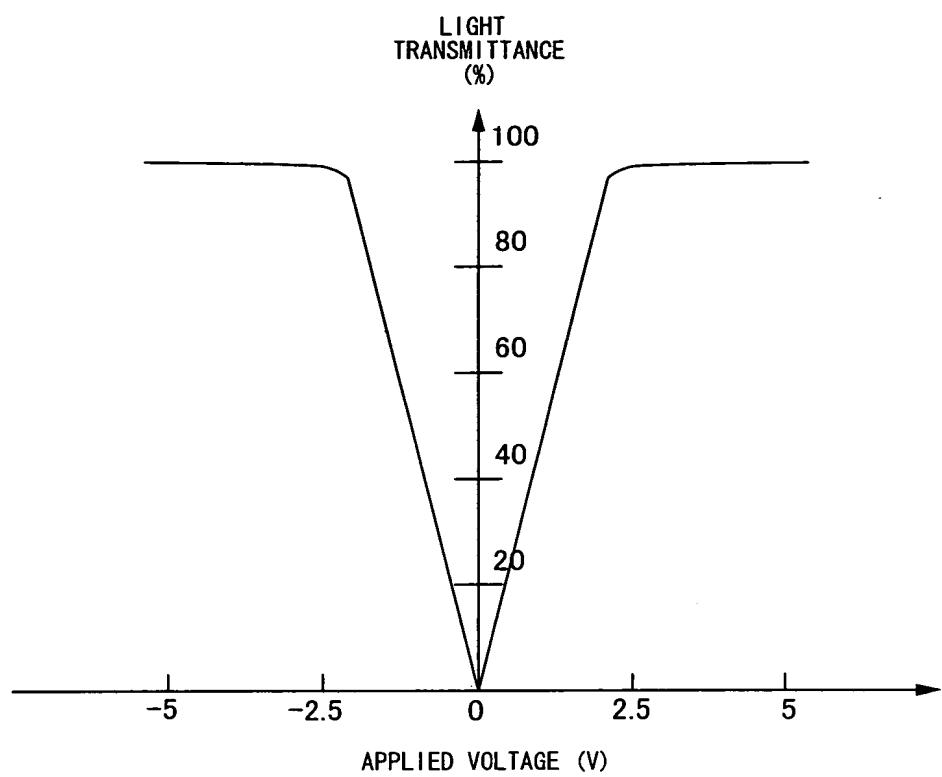
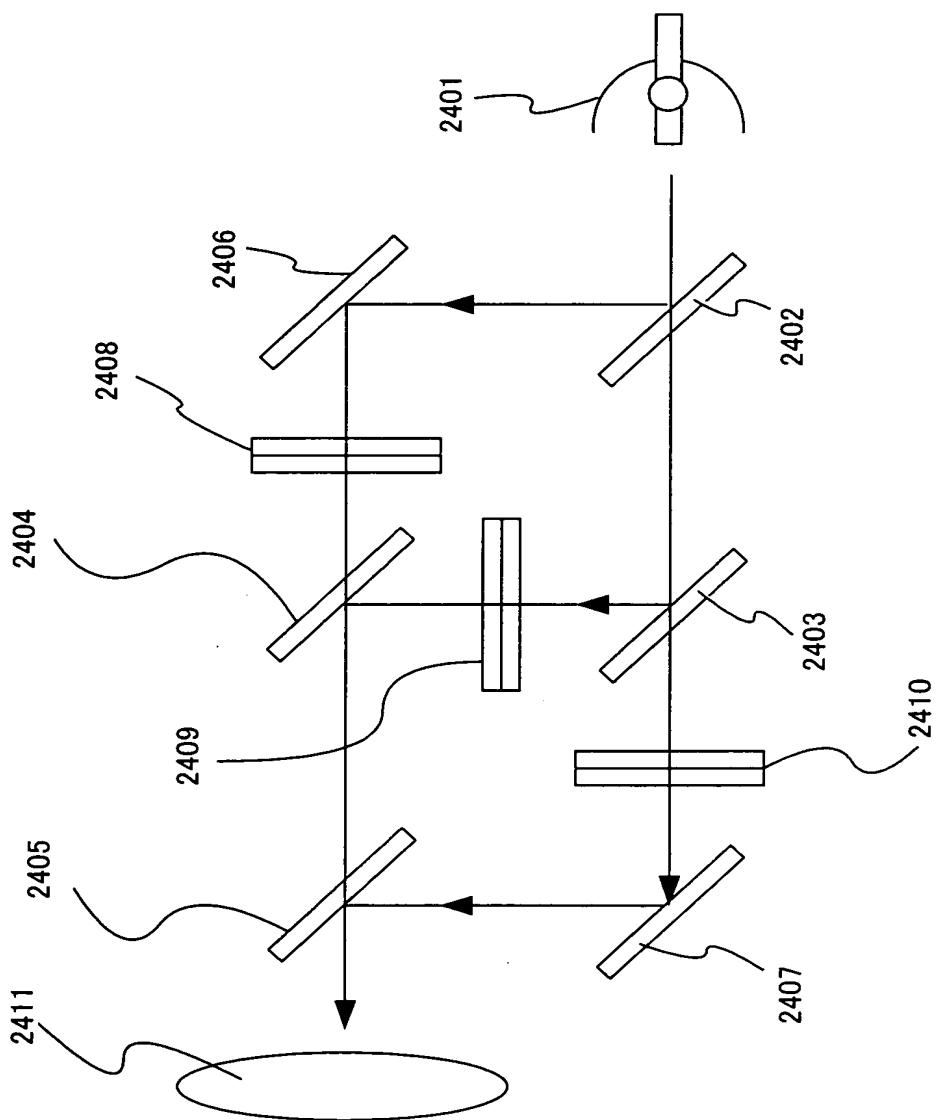
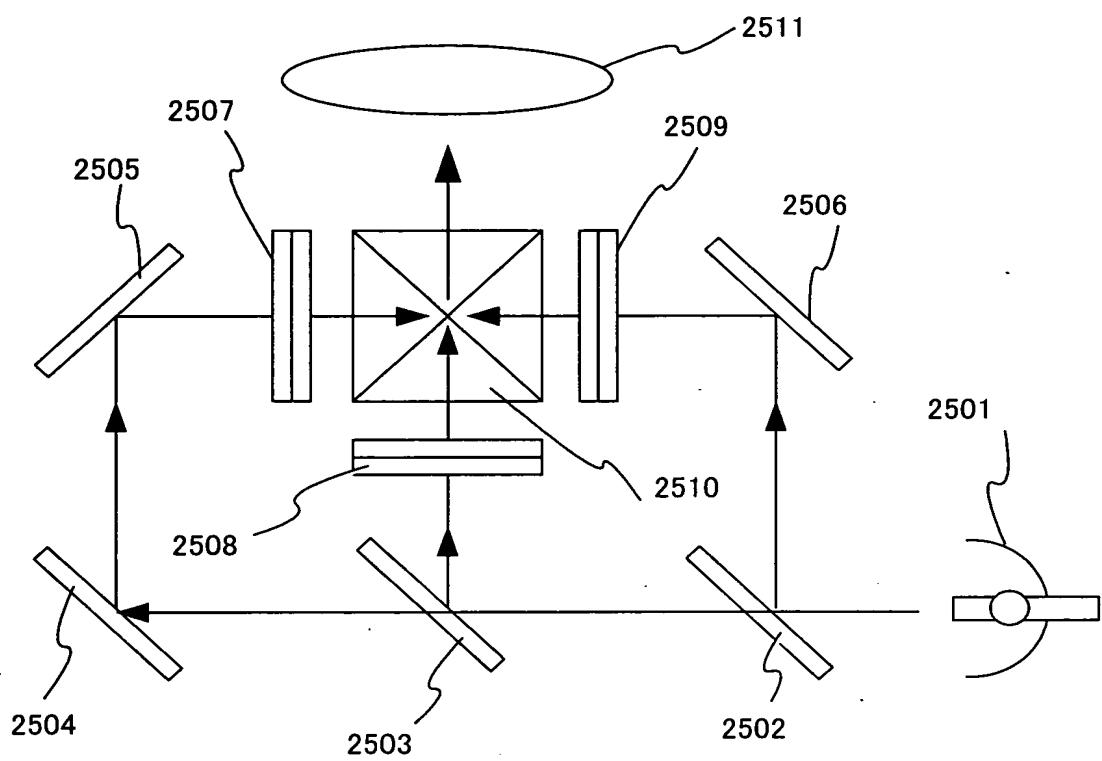


FIG. 23

FIG. 24





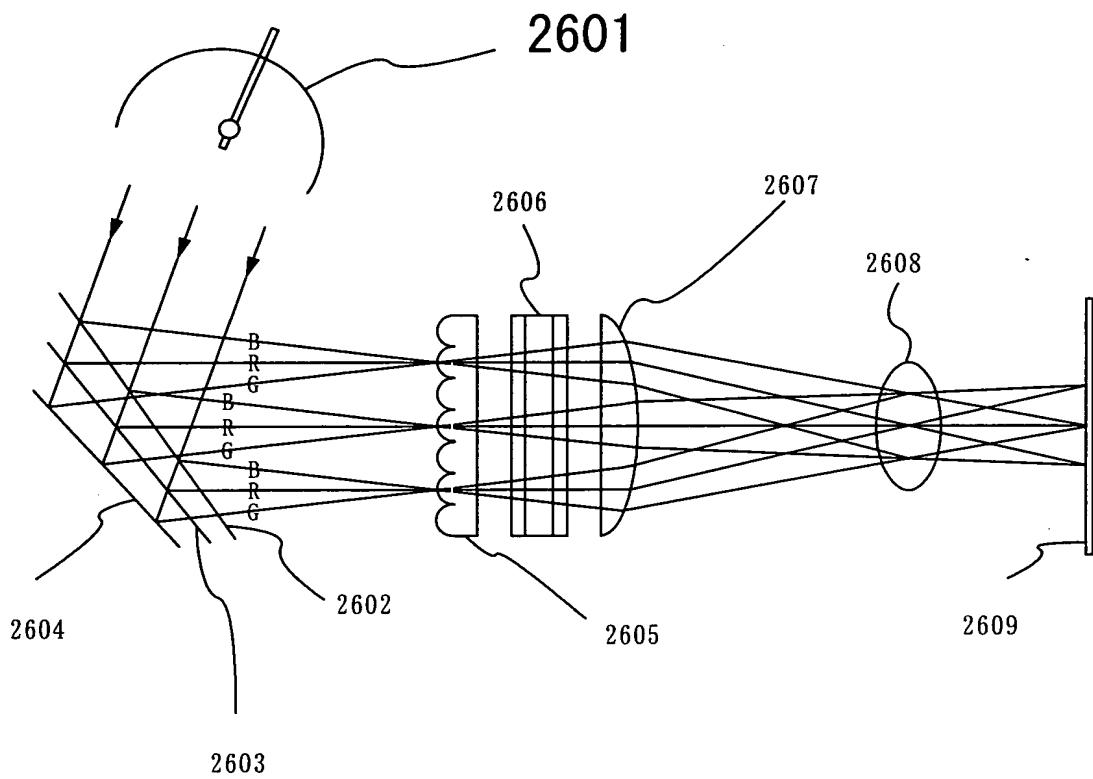


FIG. 26

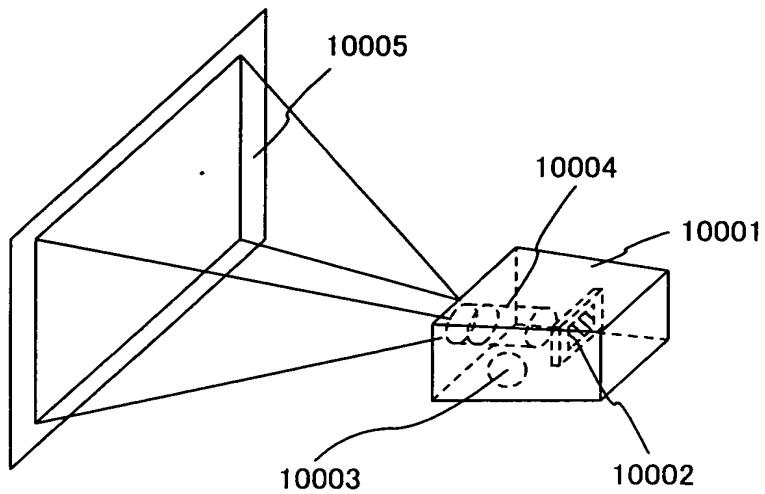


FIG. 27A

00000000000000000000000000000000

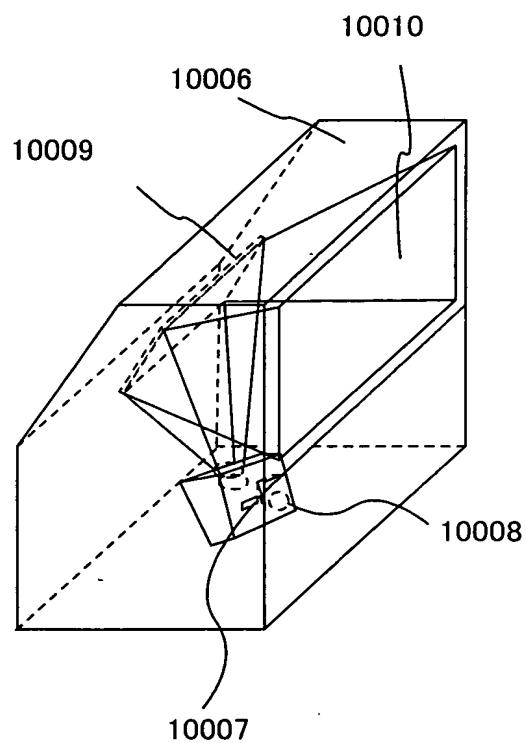


FIG. 27B

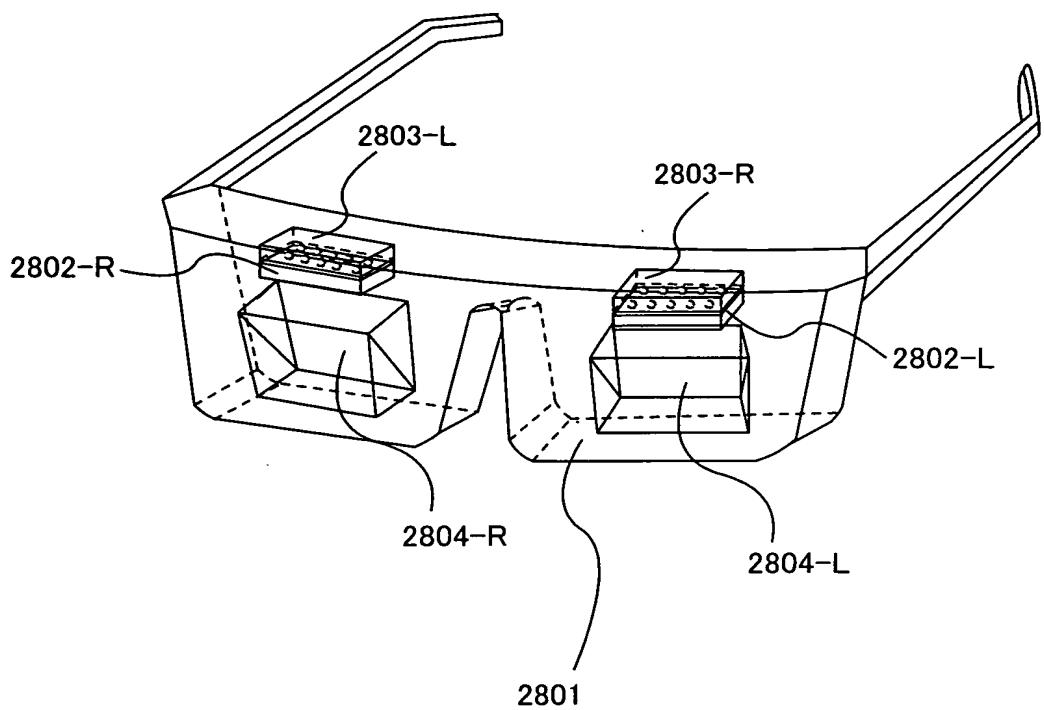


FIG. 28

VIDEO R1 G1 B1 R2 G2 B2 R3

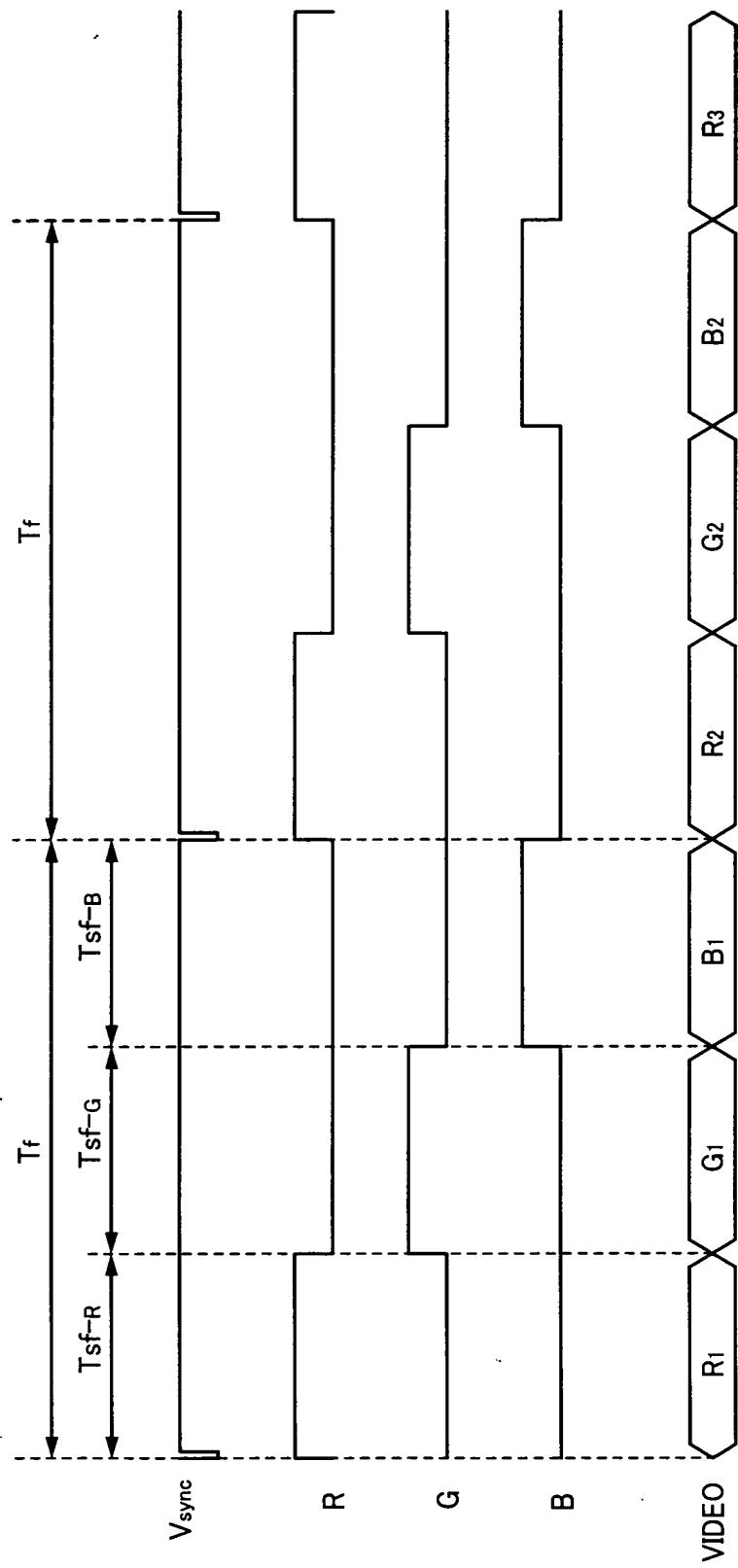


FIG. 29

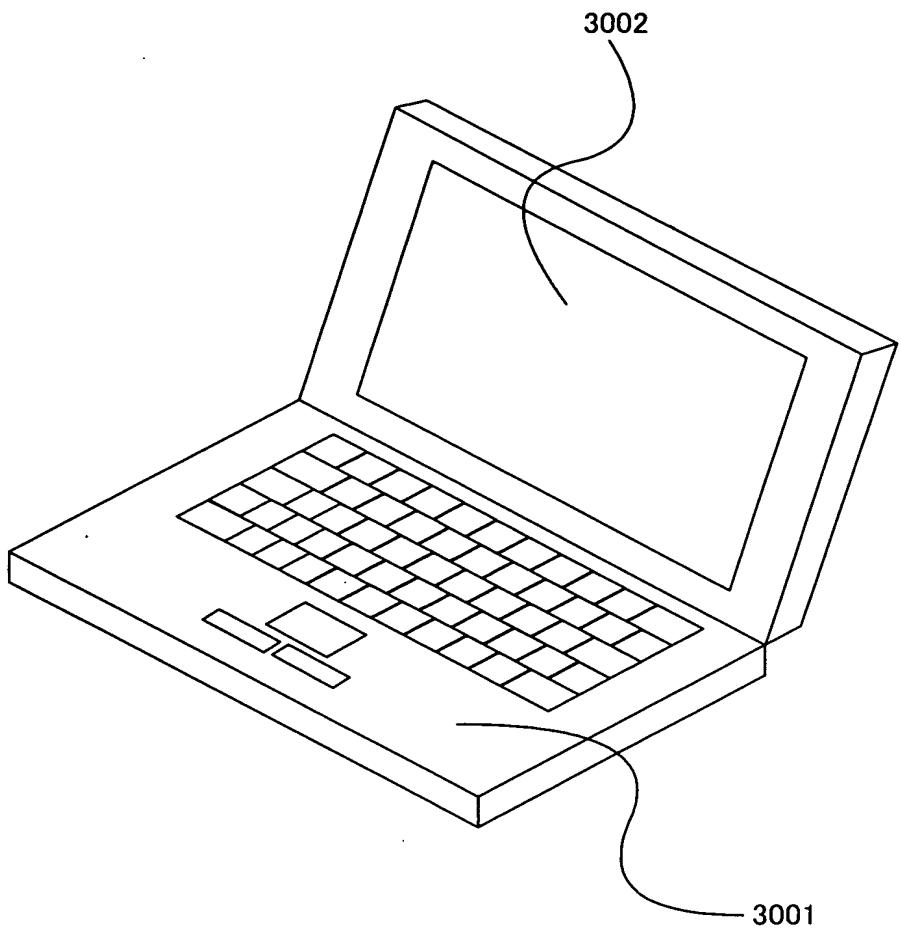
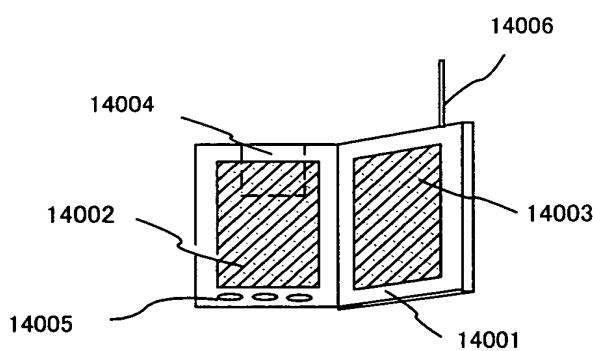
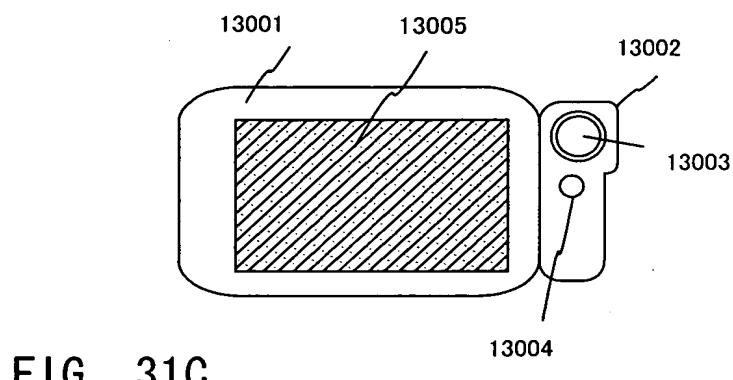
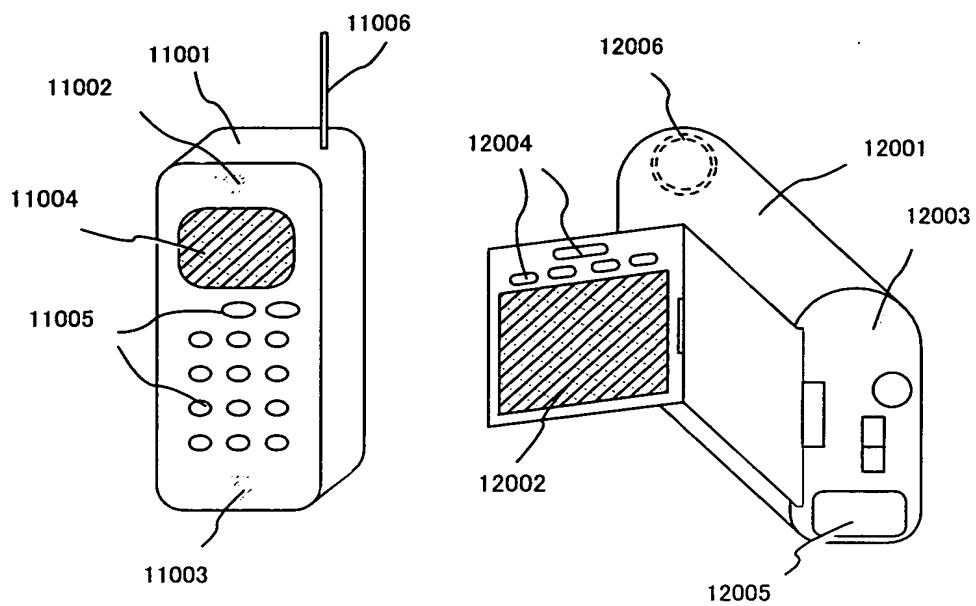


FIG. 30



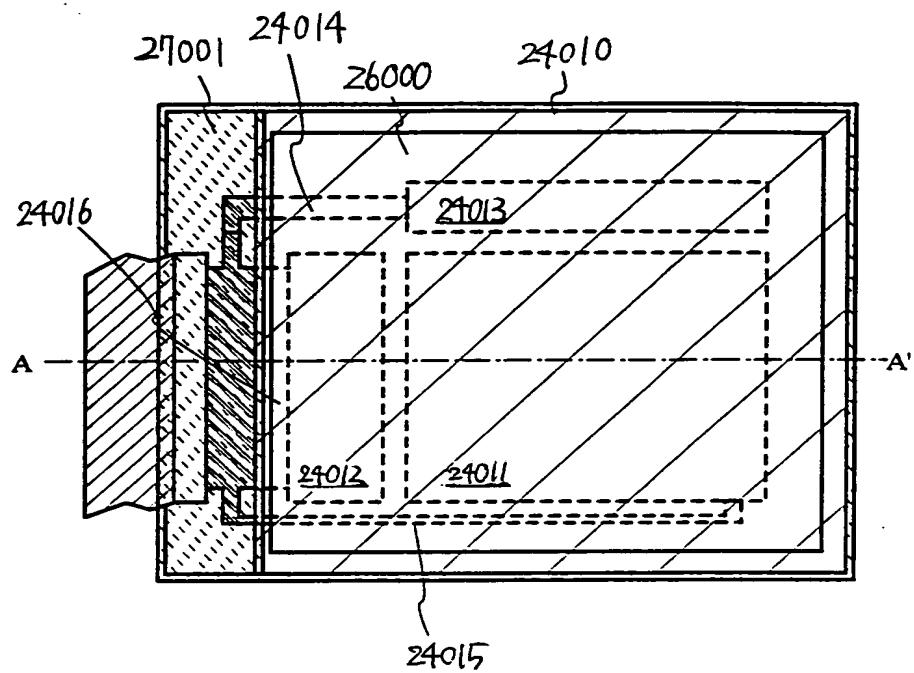


FIG. 32A

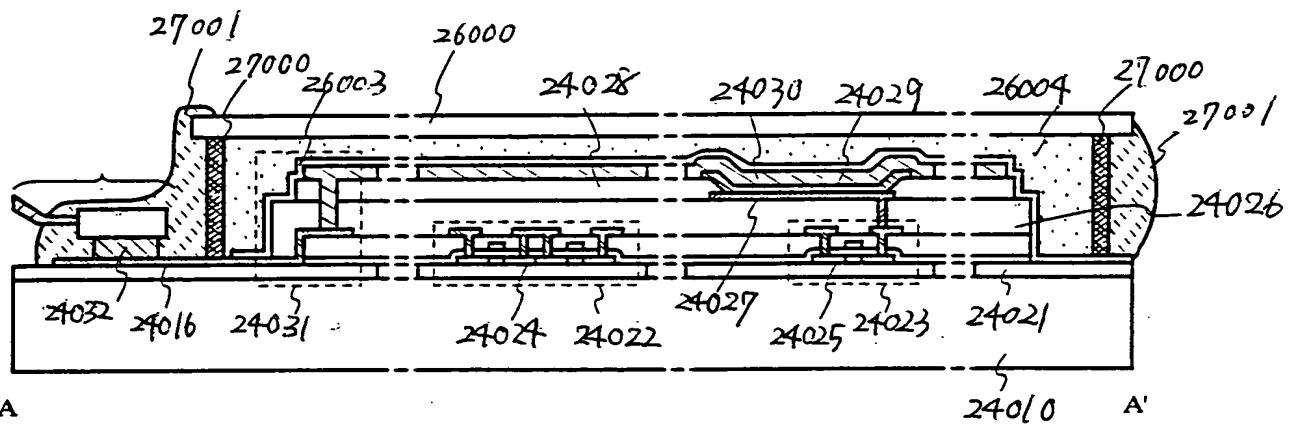


FIG. 32B

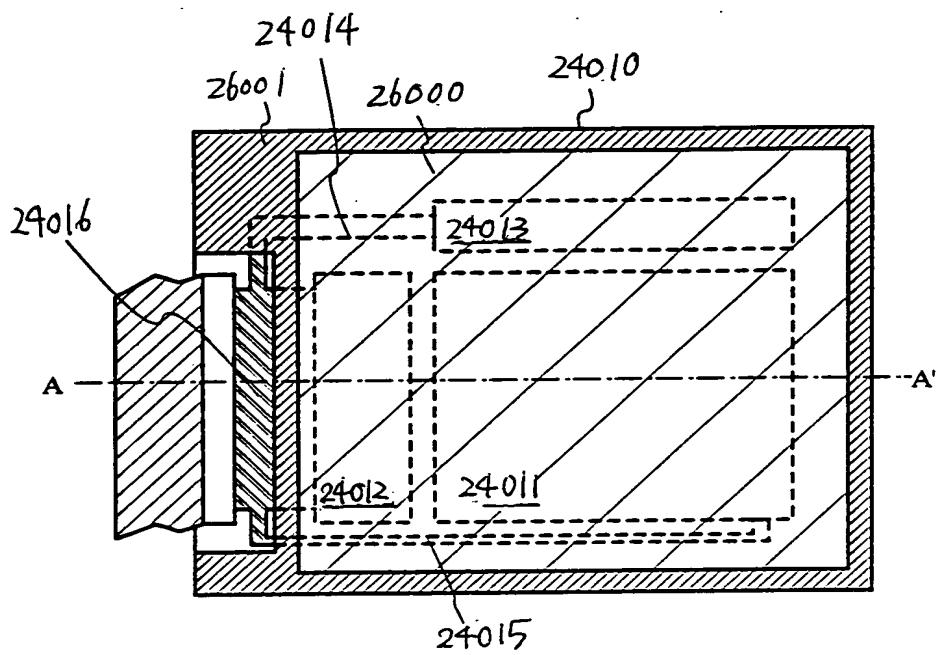


FIG. 33A

0005467825 • 022000

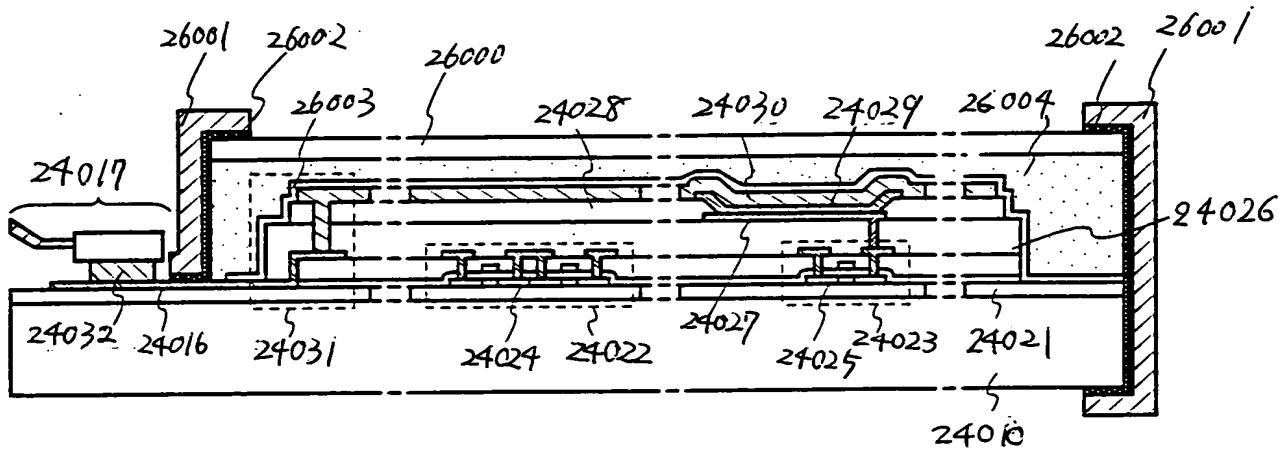
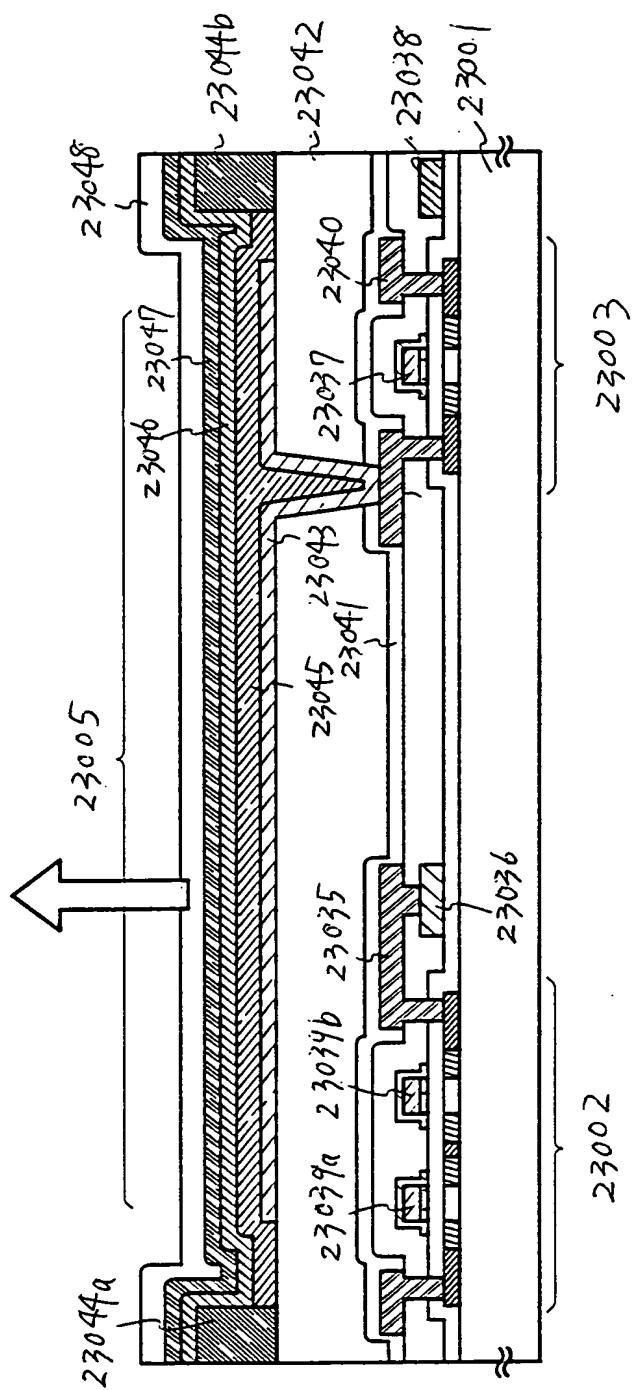


FIG. 33B

F | G. 34



00000000000000000000000000000000

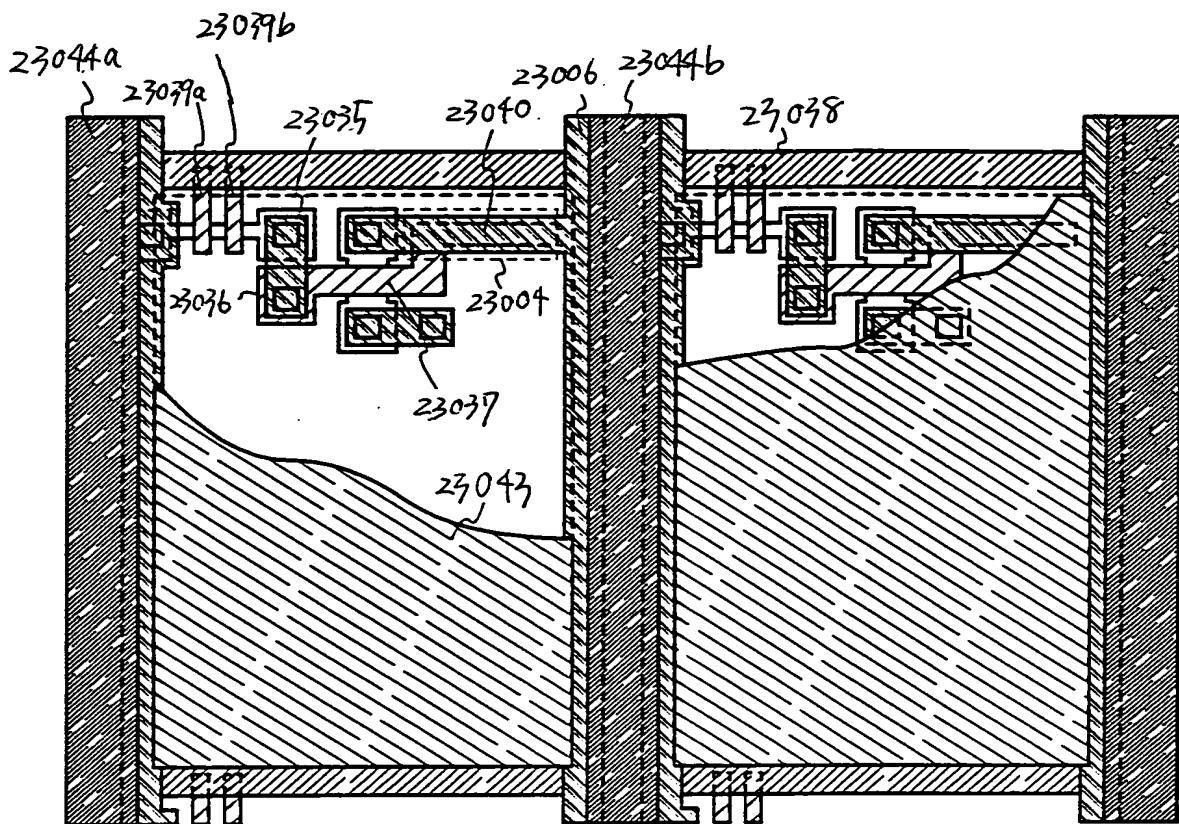


FIG. 35A

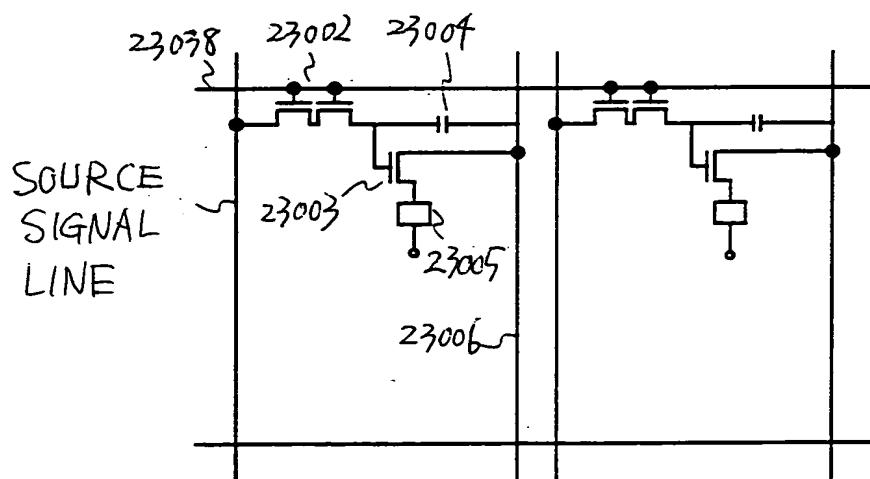


FIG. 35B

0022300500500

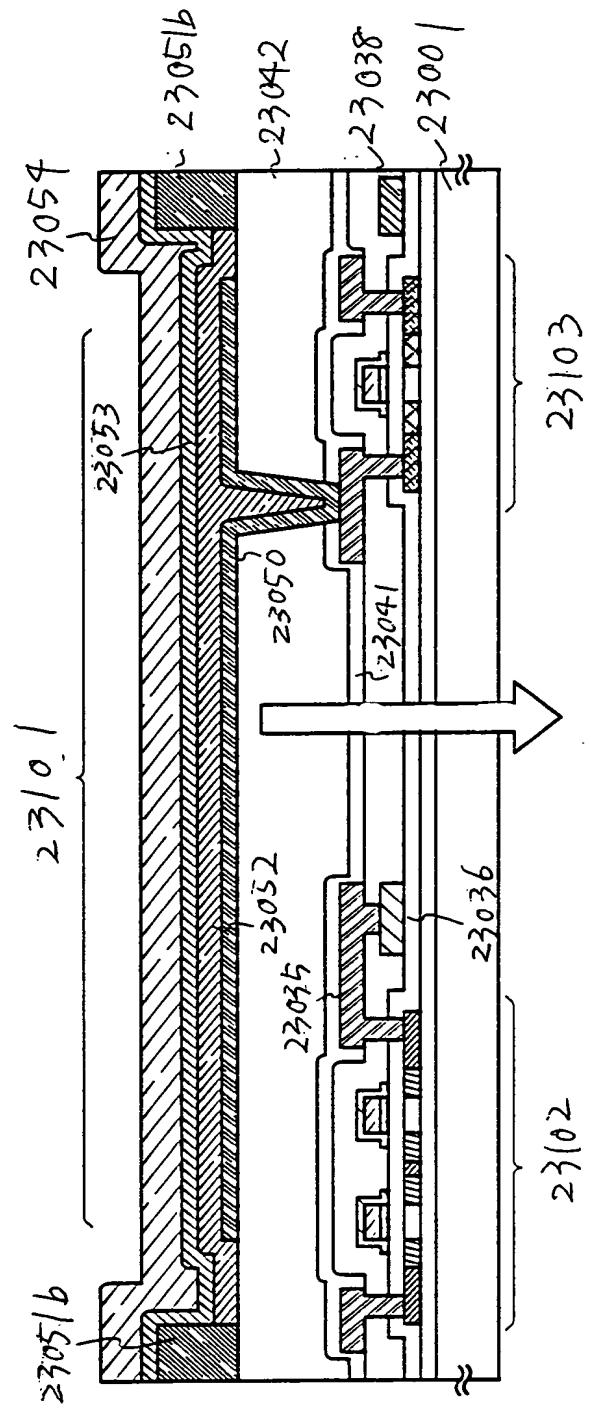


Fig. 36

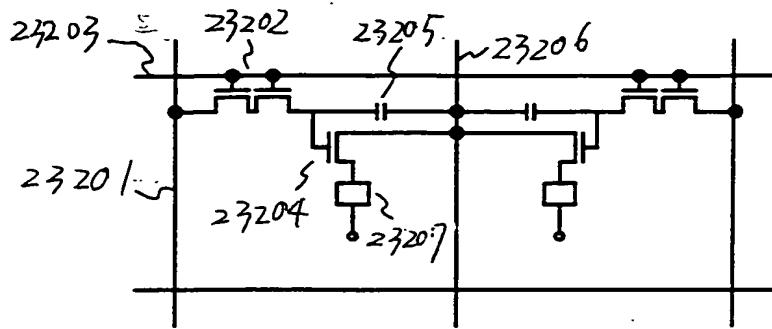


FIG. 37A

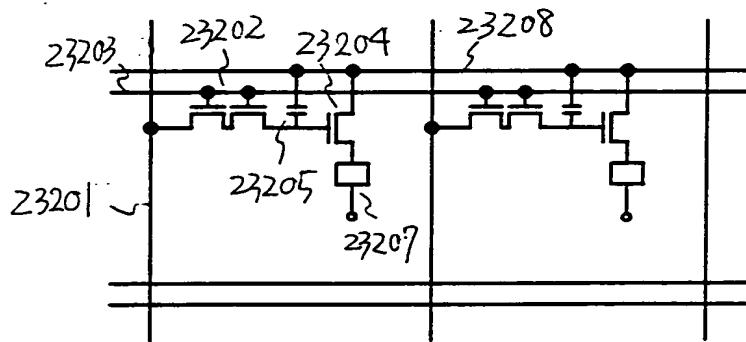


FIG. 37B

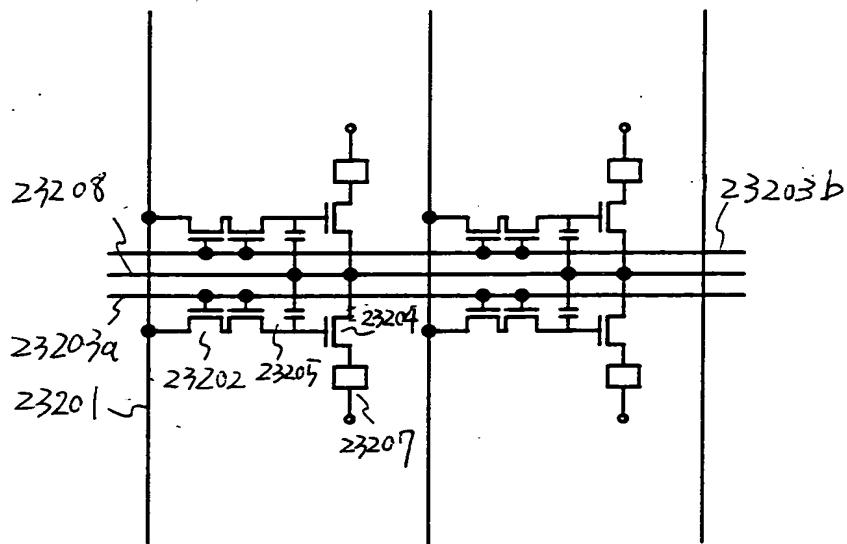


FIG. 37C